

**SPECIFICATION
FOR
LCM Module
KD056VGTPA003**

MODULE:	KD056VGTPA003
CUSTOMER:	

REV	DESCRIPTION	DATE
1.0	FIRST ISSUE	2017.11.27

STARTEK	INITIAL	DATE
PREPARED BY		
CHECKED BY		
APPROVED BY		

CUSTOMER	INITIAL	DATE
APPROVED BY		

ISO9001:2008
ISO/TS16949:2009

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*** Description**

This is a color active matrix TFT (Thin Film Transistor) LCD (liquid crystal display) that uses amorphous silicon TFT as a switching device. This model is composed of a Transmissive type TFT-LCD Panel, driver circuit, back-light unit. The resolution of a 5.6'TFT-LCD contains 640x480 pixels, and can display up to 65K/262K colors.

*** Features**

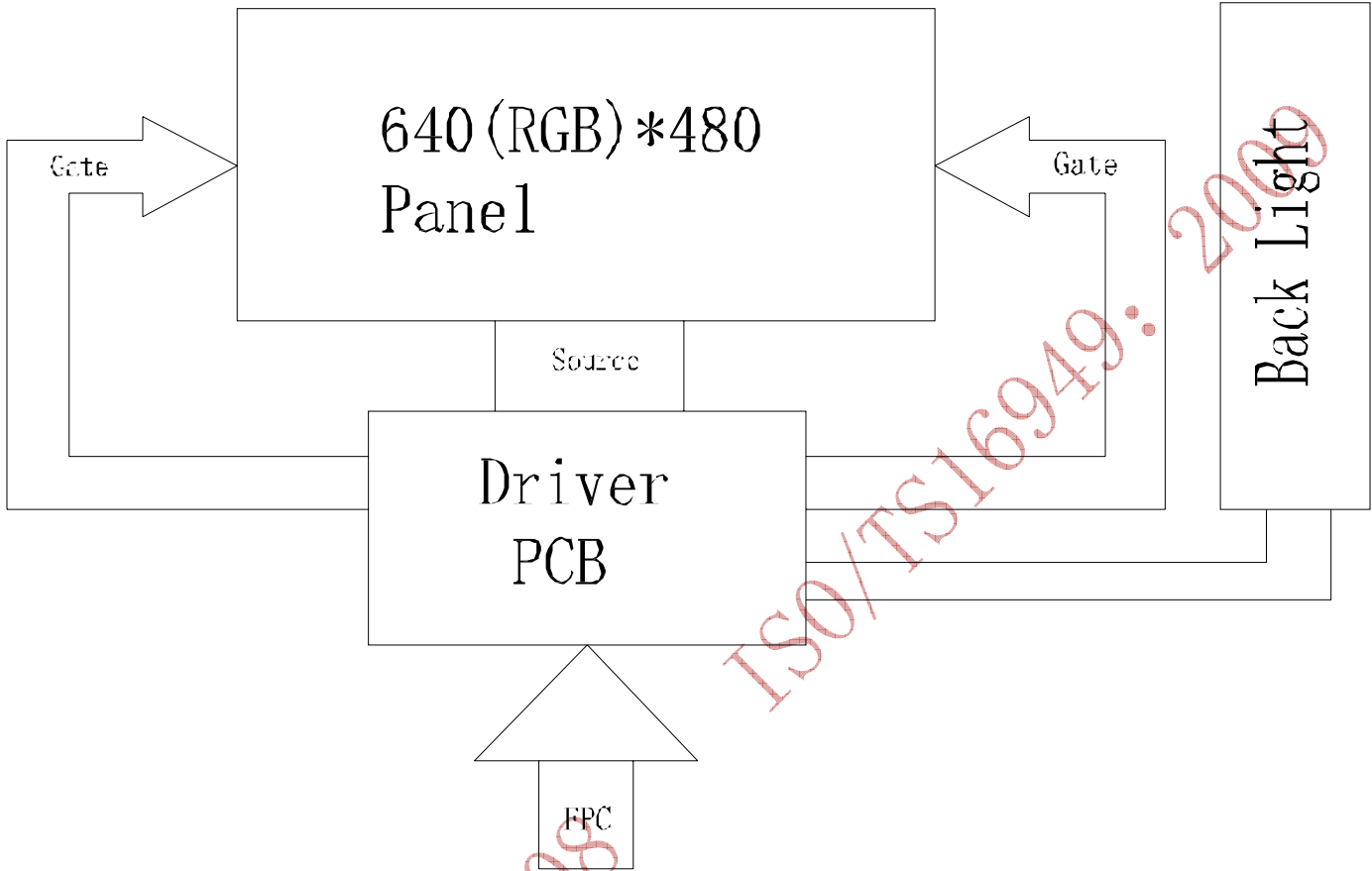
- Low Input Voltage: 3.3V(TYP)
- Display Colors of TFT LCD: 65K/262K colors
- Interface: 16/18-bits RGB interface.

General Information Items	Specification	Unit	Note
	Main Panel		
Display area(AA)	112.896(H)*84.672(V) (5.6inch)	mm	-
Driver element	TFT active matrix	-	-
Display colors	65K/262K	colors	-
Number of pixels	640(RGB)*480	dots	-
TFT Pixel arrangement	RGB vertical stripe	-	-
Pixel pitch	0.0588(H)*0.1764(V)	mm	-
Viewing angle	12:00	o'clock	-
Controller IC	---	-	-
Display mode	Transmissive/Normally white	-	-
Operating temperature	-20~+70	°C	-
Storage temperature	-30~+80	°C	-

*** Mechanical Information**

Item		Min.	Typ.	Max.	Unit	Note
Module size	Horizontal(H)		126.5		mm	-
	Vertical(V)		100		mm	-
	Depth(D)		4.5		mm	-
Weight			81		g	-

1. Block Diagram

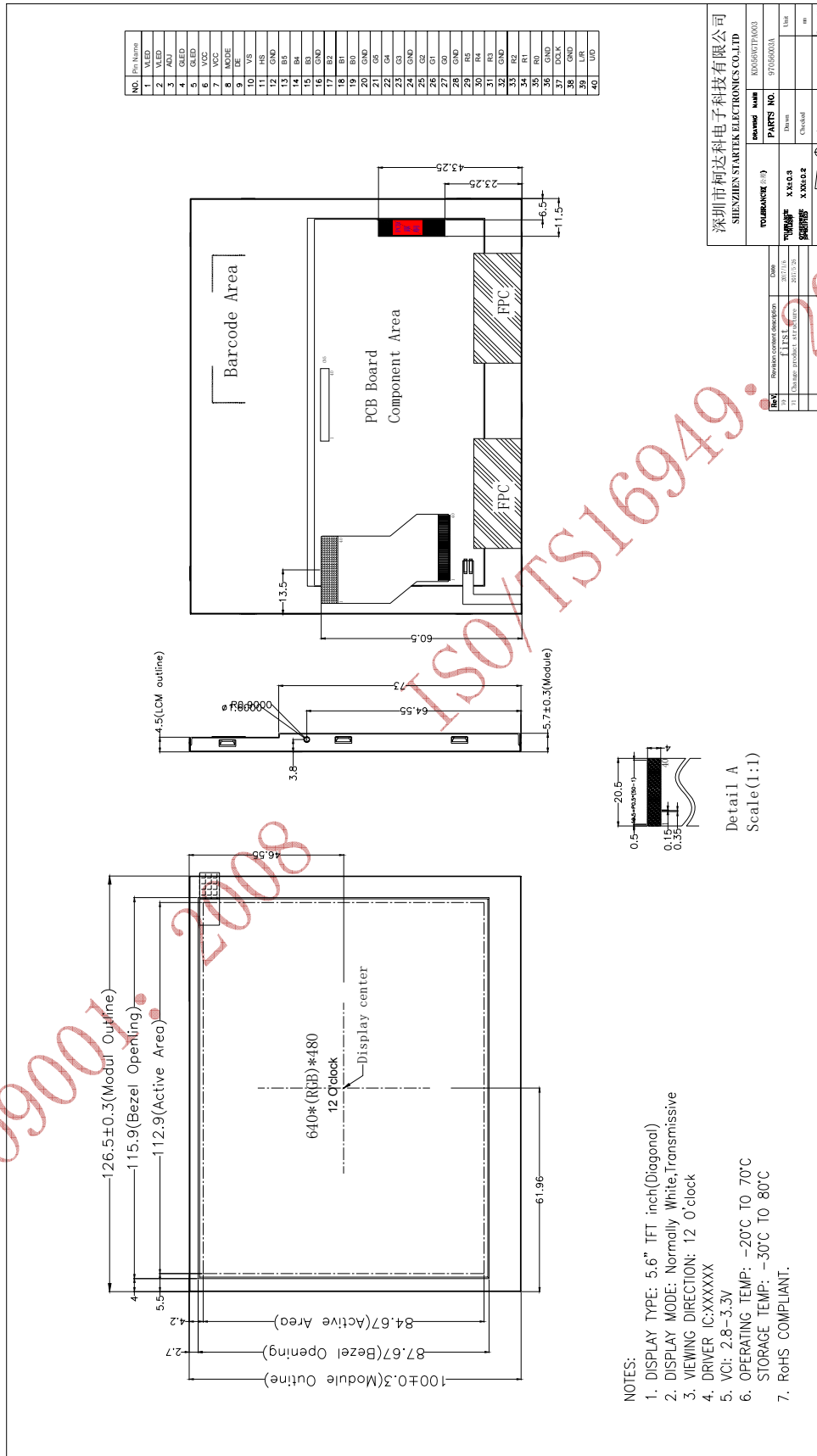


ISO 9001: 2008

ISO/TS 16949: 2009

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	常备库存 Standing Stock	长期供货 Long Time supply	支持小量 NO MOQ	品种齐全 In Full Range

2. Outline dimension



REV	Revision control description	Date
1.0	Initial product release	2012.12
深圳市柯达电子科技有限公司 SHENZHEN STARTEK ELECTRONICS CO., LTD TOLERANCE (mm) DIMENSIONS: XXX.0 DECIMALS: XXXX.2 Scale: 1:1		
Drawn	Checked	Appr.
DATE	DATE	DATE
2012.12	2012.12	2012.12
Part No	Part No	Part No
KD056VTPA003	KD056VTPA003	KD056VTPA003
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3. Input terminal Pin Assignment

NO.	SYMBOL	DISCRIPTION	I/O
1	V _{LED}	Power Voltage for LED circuit	P
2	V _{LED}	Power Voltage for LED circuit	P
3	ADJ	Adjust the LED brightness with PWM Pulse	NOTE 1.2
4	G _{LED}	Ground for LED circuit	P
5	G _{LED}	Ground for LED circuit	P
6	VCC	Power Voltage for digital circuit	P
7	VCC	Power Voltage for digital circuit	P
8	MODE	DE or HV mode control	NOTE 3
9	DE	Data Input Enable.	I
10	VS	Vertical Sync Input.	I
11	HS	Horizontal Sync Input.	I
12	GND	Power ground .	P
13	B5	Blue data input (MSB).	I
14	B4	Blue data input.	I
15	B3	Blue data input.	I
16	GND	Power ground .	P
17	B2	Blue data input.	I
18	B1	Blue data input.	I
19	B0	Blue data input(LSB)	I
20	GND	Power Ground.	P
21	G5	Green data input(MSB)	I
22	G4	Green data input	I
23	G3	Green data input	I
24	GND	Power ground .	P
25	G2	Green data input	I
26	G1	Green data input	I

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 常备库存
 Standing Stock

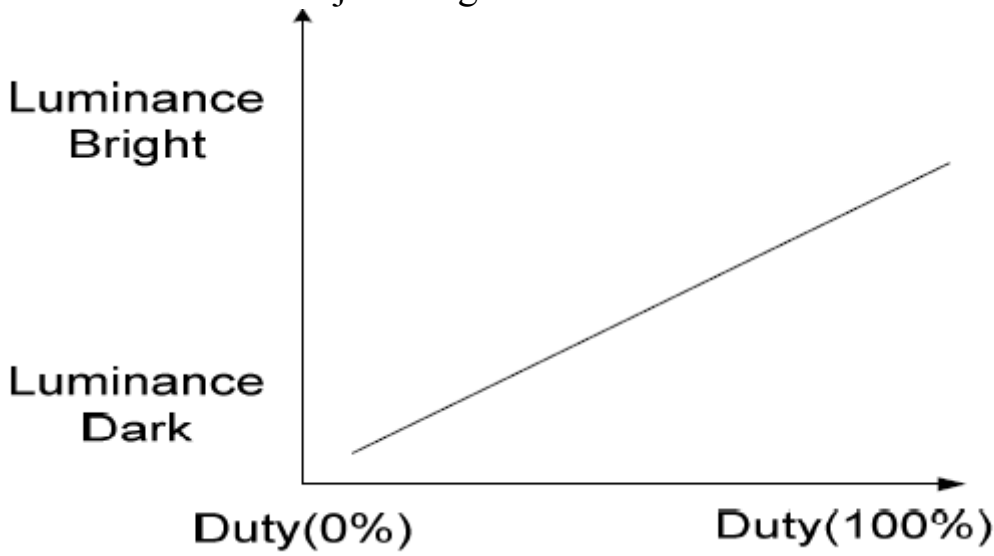
 长期供货
 Long Time supply

 支持小量
 NO MOQ

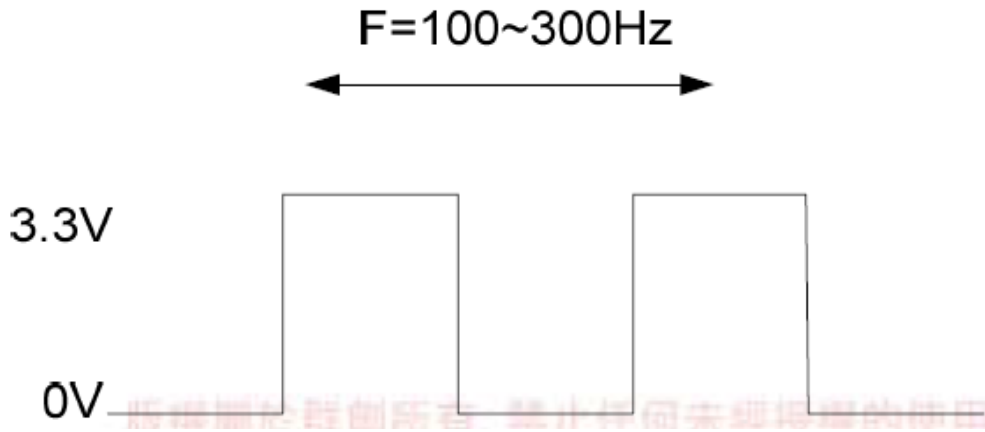
 品种齐全
 In Full Range

27	G0	Green data input(LSB)	I
28	GND	Power ground .	P
29	R5	Red data input(MSB)	I
30	R4	Red data input	I
31	R3	Red data input	I
32	GND	Power ground	P
33	R2	Red data input	I
34	R1	Red data input	I
35	R0	Red data input(LSB)	I
36	GND	Power ground	P
37	DCLK	Sample clock.	I
38	GND	Power Ground.	P
39	L/R	Select left to right scanning direction	Note4,5
40	U/D	Select up or down scanning direction	Note4,5

Notel: Pin.3 is used to adjust brightness.



Note 2: ADJ signal=0~3.3V, operation frequency: 100~300Hz



Note 3: DE Mode, Mode="H", HS floating and VS floating
 HV Mode, Mode="L" and DE floating.

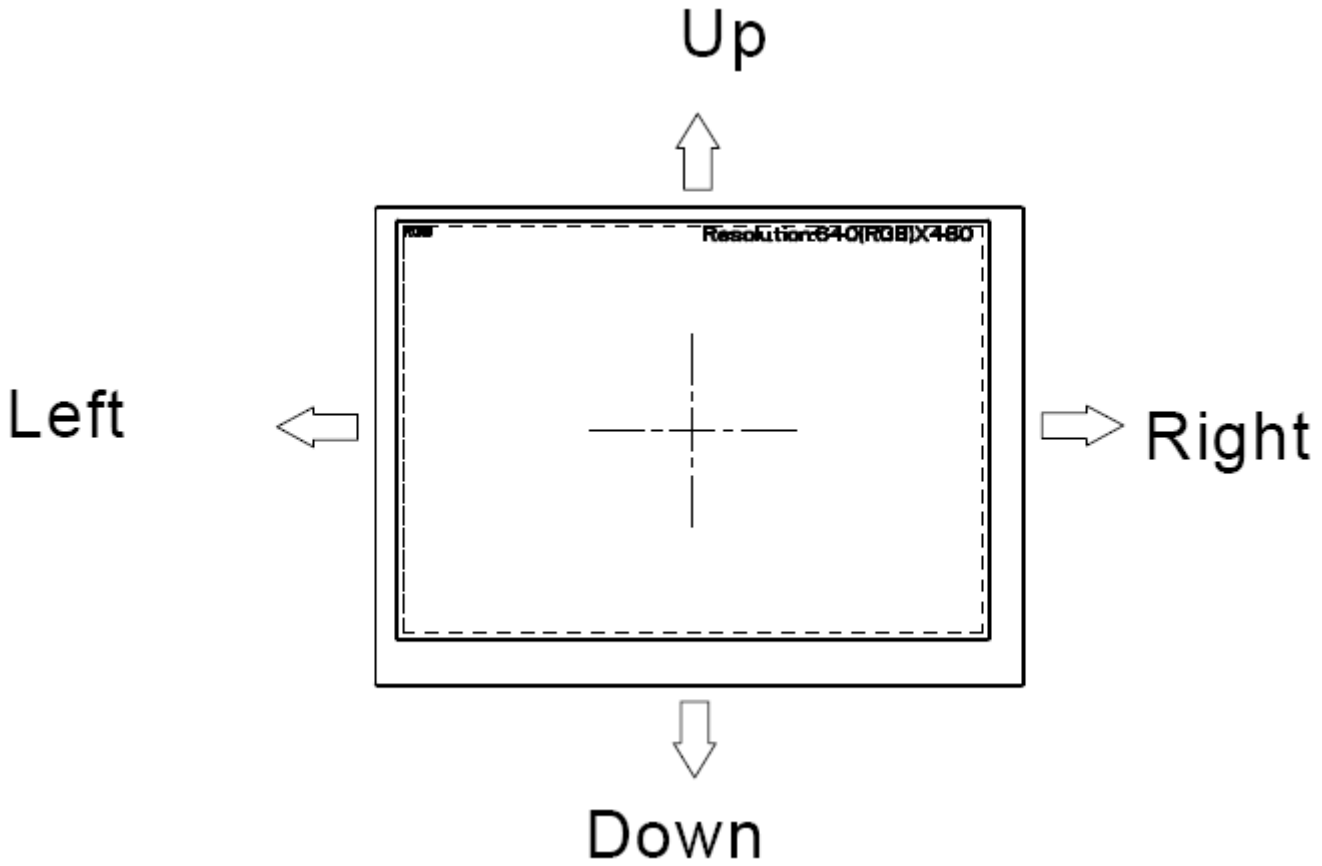
Note 4: Selection of scanning mode

Setting of scan control input		Scanning direction
U/D	L/R	
GND	V _{CC}	Up to down, left to right
V _{CC}	GND	Down to up, right to left
GND	GND	Up to down, right to left
V _{CC}	V _{CC}	Down to up, left to right

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ISO9001

Note 5: Definition of scanning direction.
Refer to the figure as below:



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4. LCD Optical Characteristics

4.1 Optical specification

Item	Symbol	Condition	Min.	Typ.	Max.	Unit.	Note	
Contrast Ratio	CR	$\Theta=0$ Normal viewing angle	400	500	--		(1)(2)	
Response time	Rising		T_R	--	10	20	msec	(1)(3)
	Falling		T_F	--	15	30		
Color gamut	S(%)			70	75	--	%	C-light
Luminance	cd/m ²			400	450	--		
Color Filter Chromaticity	White		W_X	0.269	0.309	0.349	-	(1)(4) CF glass
			W_Y	0.298	0.338	0.379		
	Red		R_X	0.563	0.583	0.603		
			R_Y	0.324	0.342	0.362		
	Green		G_X	0.317	0.337	0.357		
		G_Y	0.566	0.586	0.606			
	Blue	B_X	0.128	0.148	0.168			
		B_Y	0.008	0.010	0.020			
Viewing angle	Hor.	Θ_L	60	70	--	-	(1)(4) Measuring with Polarizer, Reference Only	
		Θ_R	60	70	--			
	Ver.	Θ_U	40	50	--			
		Θ_D	60	70	--			
Option View Direction	Free							

4.2 Measuring Condition

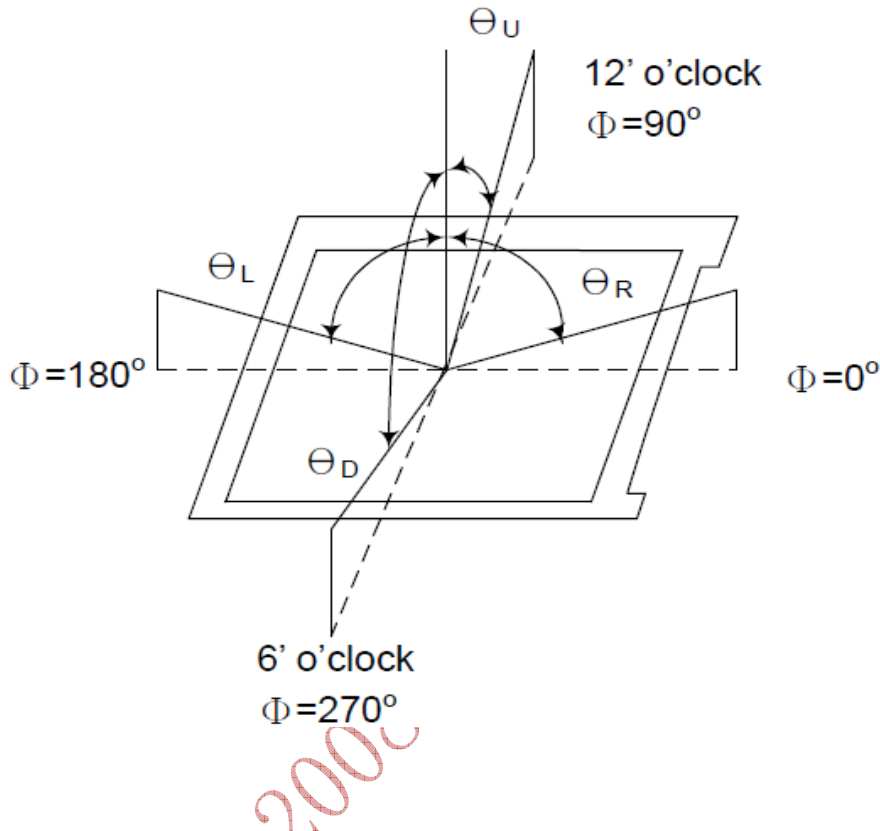
- Measuring surrounding: dark room
- Ambient temperature: 25±2℃
- 15min. warm-up time.

4.3 Measuring Equipment

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常备库存 Standing Stock	长期供货 Long Time supply	支持小量 NO MOQ	品种齐全 In Full Range	

- FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.

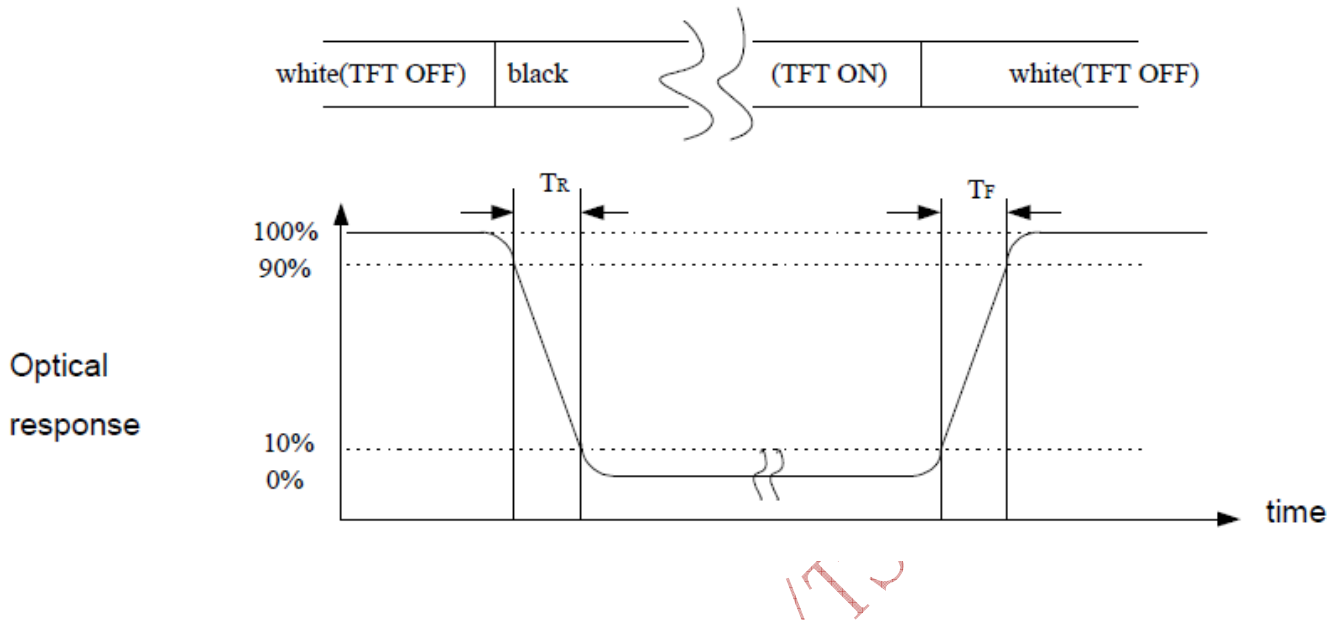
Note (1) Definition of Viewing Angle:



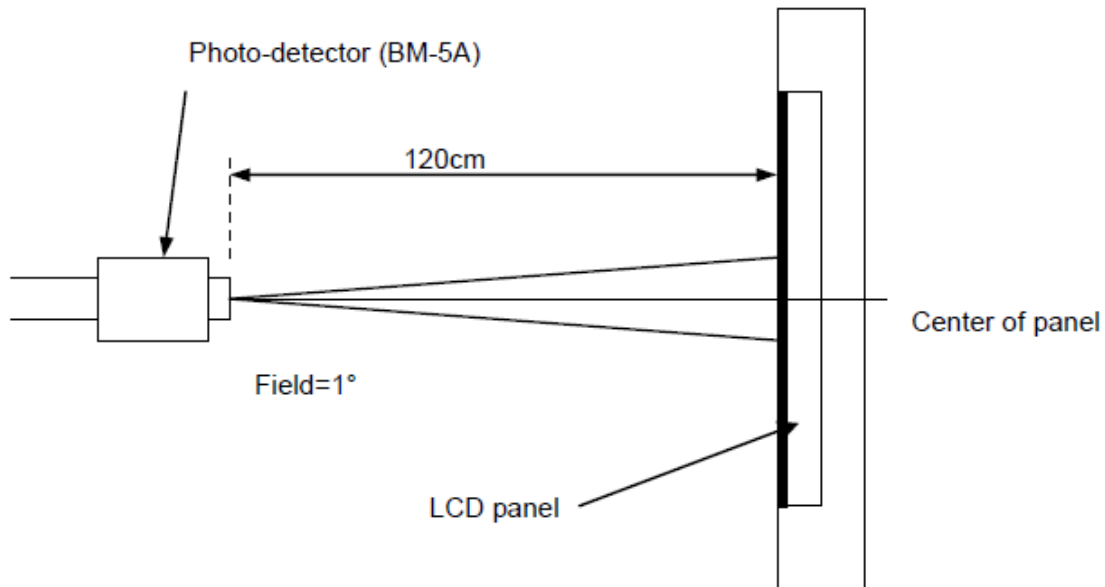
Note (2) Definition of Contrast Ratio (CR) :
measured at the center point of panel

$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

Note (3) Definition of Response Time : Sum of T_R and T_F



Note (4) Definition of optical measurement setup



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常备库存
Standing Stock

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Long Time supply

支持少量
NO MOQ

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5. TFT Electrical Characteristics

5.1 Absolute Maximum Rating (Ta=25 VSS=0V)

Characteristics	Symbol	Min.	Max.	Unit
Supply Voltage	V _{CC}	-0.3	6.5	V
	V _{LED}	-0.3	6.5	V
Operating temperature	T _{OP}	-20	+70	°C
Storage temperature	T _{ST}	-30	+80	°C

NOTE: If the absolute maximum rating of even is one of the above parameters is exceeded even momentarily, the quality of the product may be degraded. Absolute maximum ratings, therefore, specify the values exceeding which the product may be physically damaged. Be sure to use the product within the range of the absolute maximum ratings.

5.2 DC Electrical Characteristics

Characteristics	Symbol	Min.	Typ.	Max.	Unit	Note
Digital Supply Voltage	V _{CC}	3.1	3.3	3.5	V	Note1
	V _{LED}	4.8	5.0	5.2	V	Note2
Normal mode Current consumption	I _{CC}	--	200	250	mA	
	I _{LED}	--	350	480	mA	Note3
Level input voltage	V _{IH}	0.7V _{CC}	--	1V _{CC}	V	Note4
	V _{IL}	0	--	0.3V _{CC}	V	
LED life time	--	20,000	--	--	Hr	Note 5

Note 1: VCC setting should match the signals output voltage (refer to Note 4) of customer's system board.

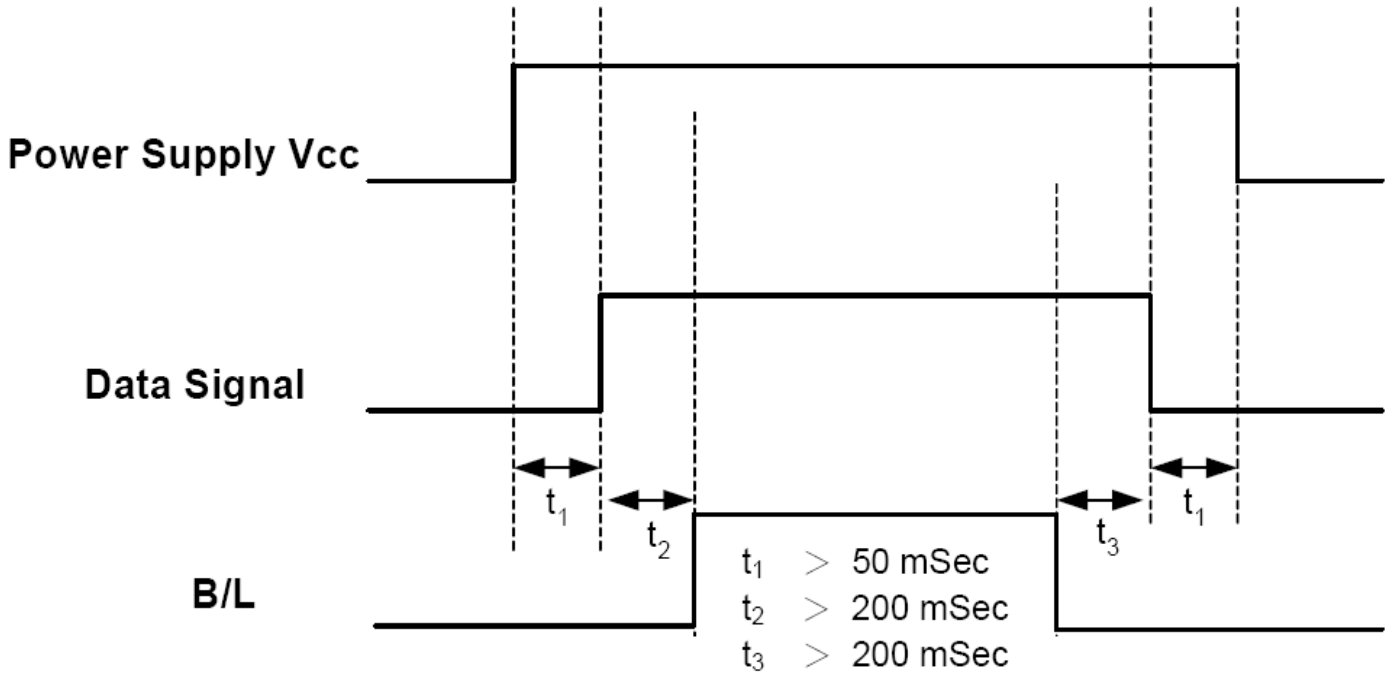
Note 2: LED driving voltage.

Note 3: LED driving current.

Note 4: DCLK, DE, HS, VS, R0~R5, G0~G5, B0~B5.

Note 5: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25°C and VLED=5.0V. The LED lifetime could be decreased if operating VLED is larger than 5.0V.

5.3 Power Sequence



Note: Data includes DE, VS, HS, B0~B5, G0~G5, R0~R5, DCLK.

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6. TFT AC Characteristic

6.1 Timing Conditions

Input/Output Timing

Item	Symbol	Values			Unit.	Remark
		Min.	Typ.	Max.		
PXLCLK clock time	Tclk	33.3	39.7	-	ns	
PXLCLK pulse duty	Tcwh	40	50	60	%	Tclk
DATA set-up time	Tdsu	12	-	-	ns	DATA to PXLCLK
DATA hold time	Tdhd	12	-	-	ns	DATA to PXLCLK
DE setup time	Tesu	12	-	-	ns	DE to PXLCLK
VSYNC setup time	Tvst	12	-	-	ns	
VSYNC hold time	Tvhd	12	-	-	ns	
HSYNC setup time	Thst	12	-	-	ns	
HSYNC hold time	Thhd	12	-	-	ns	
HSYNC period time	Th	22.91	31.76	-	us	
HSYNC width	Thwh	1	-	-	Tclk	
VSYNC width	Tvwh	1	-	-	Th	
HSYNC to CLKIN	Thc	-	-	1	Tclk	

DE Mode input Timing Limitation

DE Mode	Values			Unit	Remark
	Min.	Typ.	Max.		
THC	48	160	765	tclk	
THD	640	640	640	tclk	
TH	688	800	1405	tclk	1TH=1line
TVC	6	45	255	line	
TVD	480	480	480	line	
TV	486	525	735	line	1TV=1field

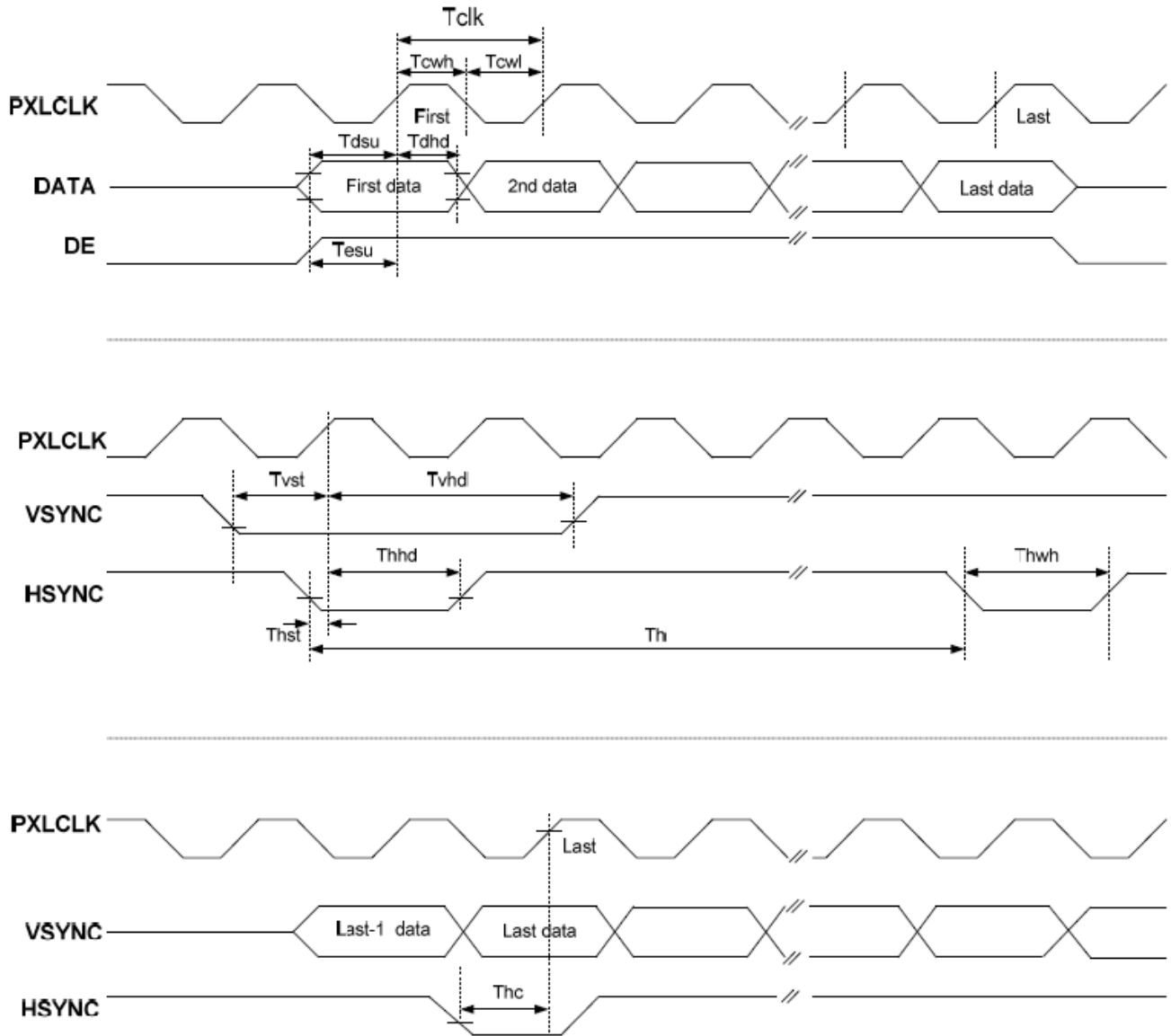
HV Mode input Timing Limitation

HV Mode	Values			Unit	Remark
	Min.	Typ.	Max.		
Thwh	-	10	-	clk	
Thbp	-	134	-	clk	
Thfp	-	16	-	clk	
THD	-	640	-	clk	
TH	-	800	-	clk	1TH=1 line
Tvwh	-	2	-	line	
Tvbp	-	11	-	line	
Tvfp	-	32	-	line	
TVD	-	480	-	line	
TV	-	525	-	line	1TV=1 field

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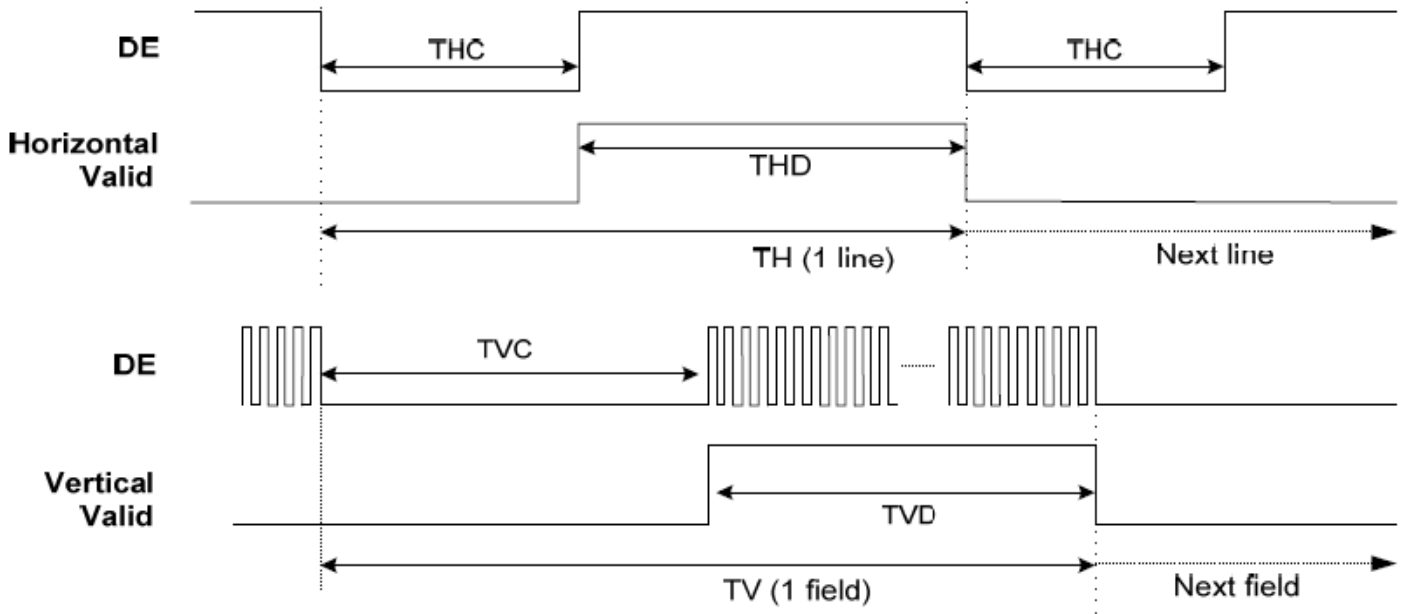
6.2 Timing Diagram



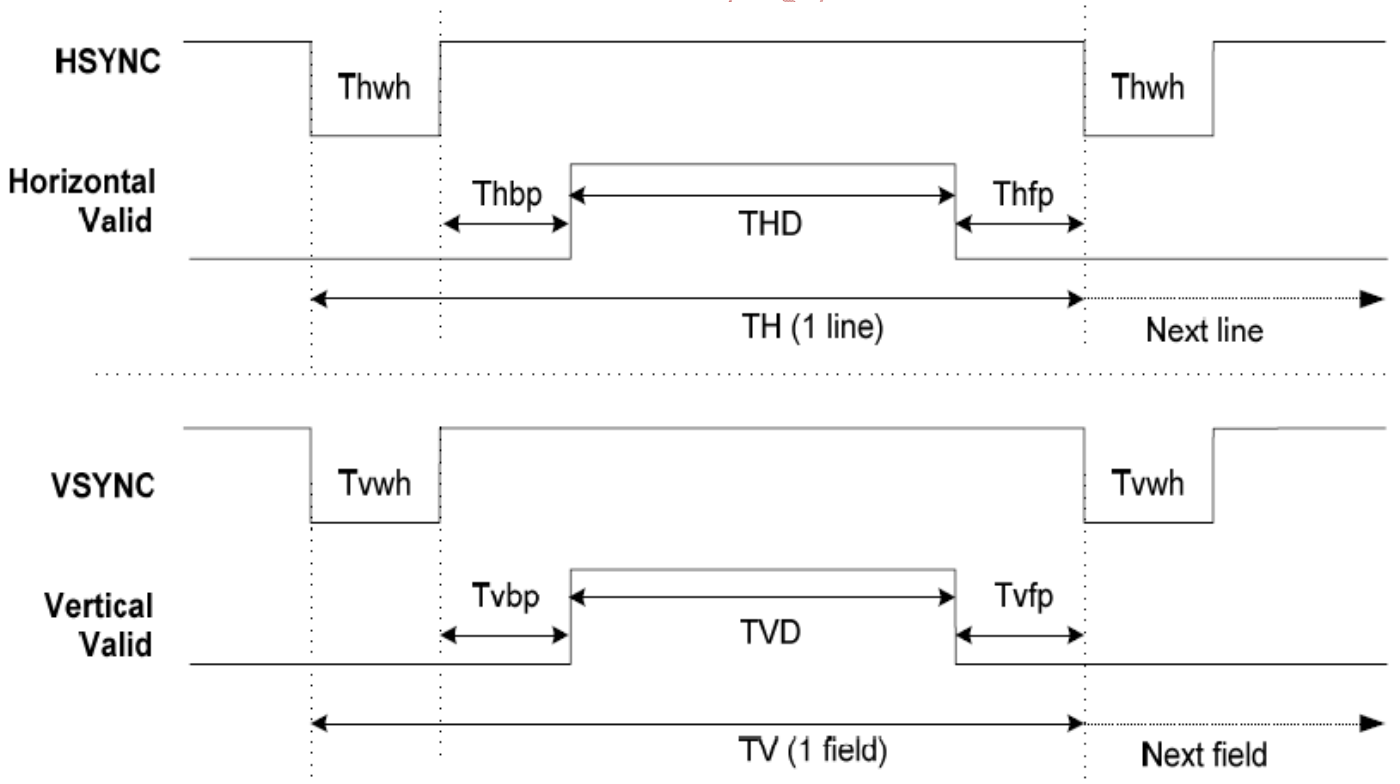
Clock and Data Input Timing Diagram

ISO9001

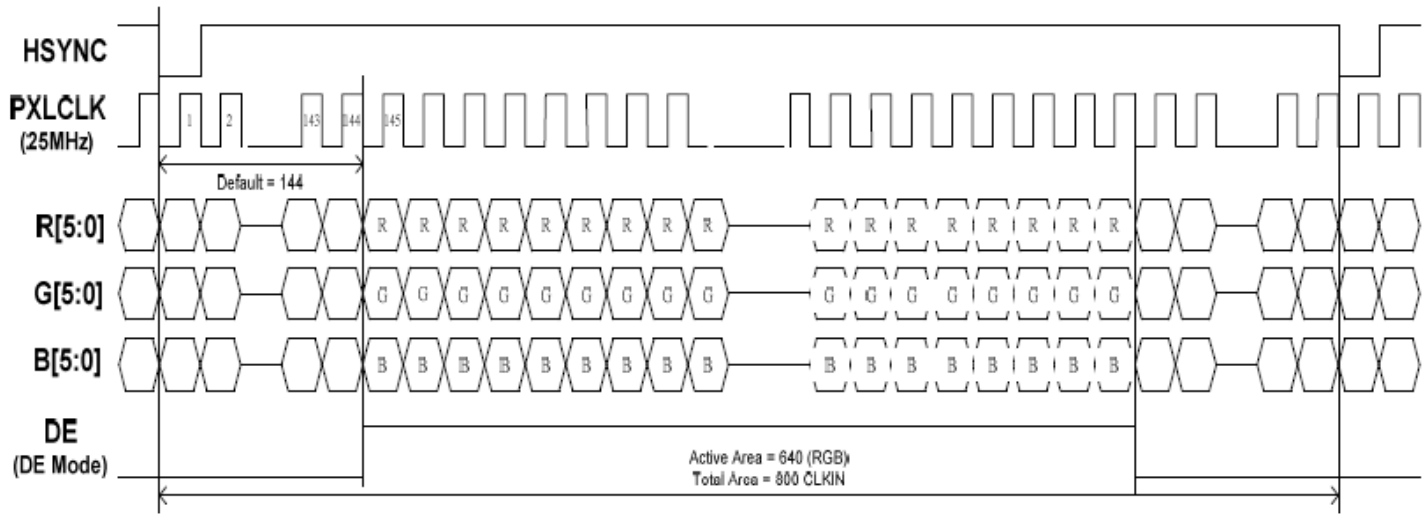
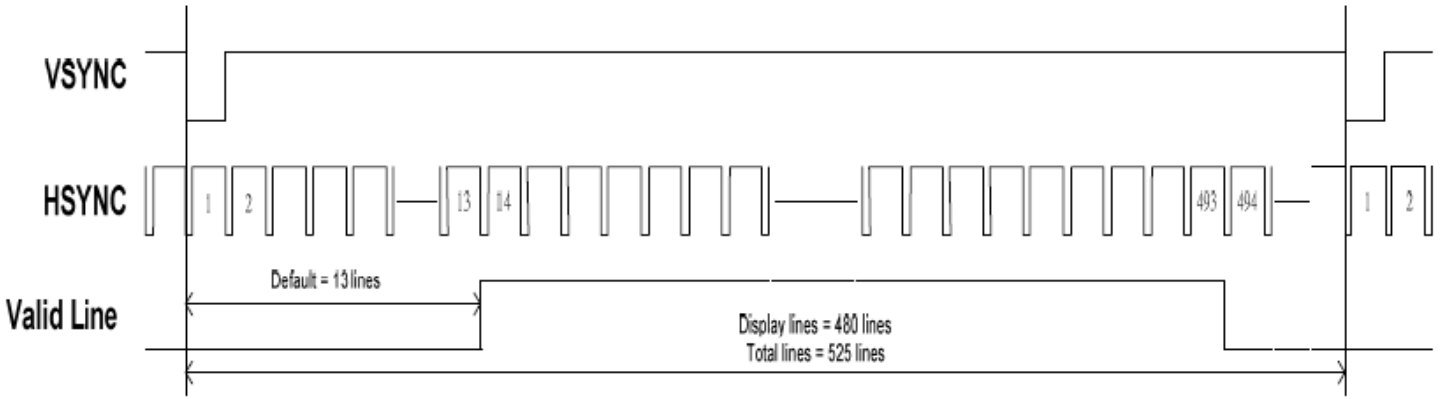
***DE Mode Input Timing**



***HV Mode Input Timing**



* RGB mode for 640 x (RGB) x 480



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7. LCD Module Out-Going Quality Level

7.1 VISUAL & FUNCTION INSPECTION STANDARD

7.1.1 Inspection conditions

Inspection performed under the following conditions is recommended.

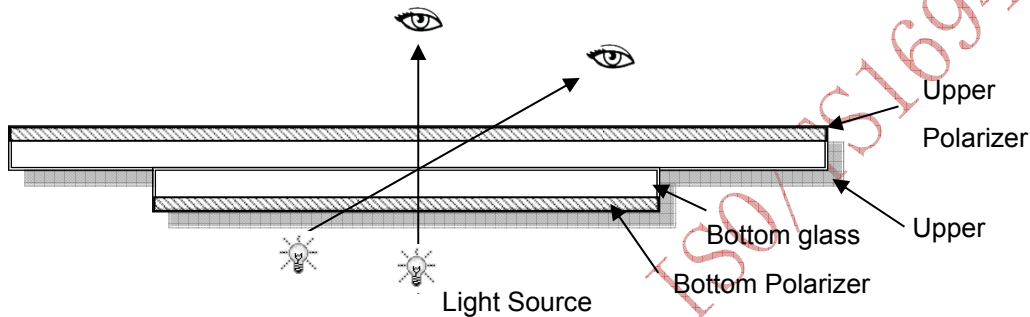
Temperature : 25±5℃

Humidity : 65%±10%RH

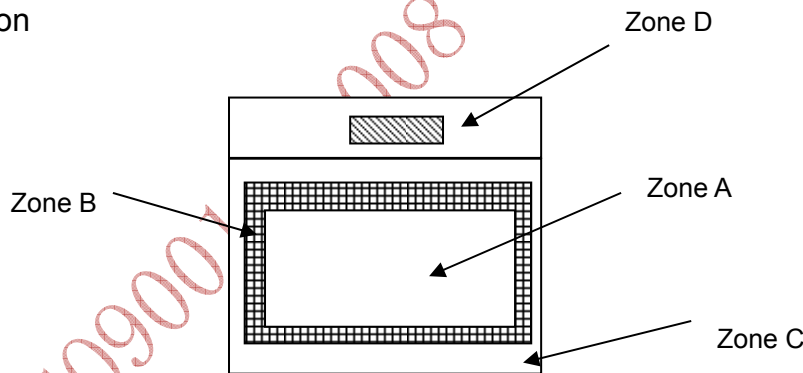
Viewing Angle : Normal viewing Angle.

Illumination: Single fluorescent lamp (300 to 700Lux)

Viewing distance:30-50cm



7.1.2 Definition



Zone A : Effective Viewing Area(Character or Digit can be seen)

Zone B : Viewing Area except Zone A

Zone C : Outside (Zone A+Zone B) which can not be seen after assembly by customer .)

Zone D : IC Bonding Area

Note:

As a general rule ,visual defects in Zone C can be ignored when it doesn't effect product function or appearance after assembly by customer

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7.1.3 Sampling Plan

According to GB/T 2828-2003 ; , normal inspection, Class II

AQL:

Major defect	Minor defect
0.65	1.5

LCD: Liquid Crystal Display , TP: Touch Panel , LCM: Liquid Crystal Module

No	Items to be inspected	Criteria	Classification of defects
1	Functional defects	1) No display, Open or miss line 2) Display abnormally, Short 3) Backlight no lighting, abnormal lighting. 4) TP no function	Major
2	Missing	Missing component	
3	Outline dimension	Overall outline dimension beyond the drawing is not allowed	
4	Color tone	Color unevenness, refer to limited sample	Minor
5	Spot Line defect	Light dot, Dim spot, Polarizer Bubble ; Polarizer accidented spot.	
6	Soldering appearance	Good soldering , Peeling off is not allowed.	
7	LCD/Polarizer/TP	Black/White spot/line, scratch, crack, etc.	

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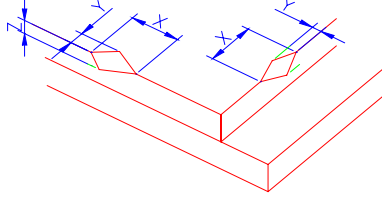
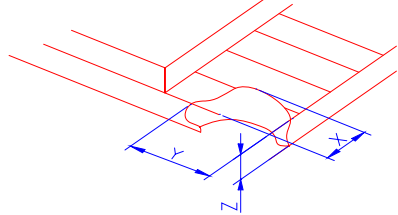
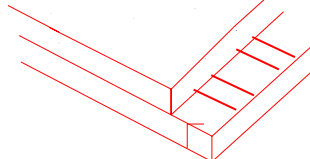
常备库存
Standing Stock

长期供货
Long Time supply

支持小量
NO MOQ

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7.1.4 Criteria (Visual)

Number	Items	Criteria(mm)						
1.0 LCD Crack/Broken NOTE: X: Length Y: Width Z: Height L: Length of ITO, T: Height of LCD	(1) The edge of LCD broken	 <table border="1" data-bbox="758 672 1452 817"> <tr> <td>X</td> <td>Y</td> <td>Z</td> </tr> <tr> <td>≤3.0mm</td> <td><Inner border line of the seal</td> <td>≤T</td> </tr> </table>	X	Y	Z	≤3.0mm	<Inner border line of the seal	≤T
X	Y	Z						
≤3.0mm	<Inner border line of the seal	≤T						
	(2)LCD corner broken	 <table border="1" data-bbox="837 1131 1372 1220"> <tr> <td>X</td> <td>Y</td> <td>Z</td> </tr> <tr> <td>≤3.0mm</td> <td>≤L</td> <td>≤T</td> </tr> </table>	X	Y	Z	≤3.0mm	≤L	≤T
X	Y	Z						
≤3.0mm	≤L	≤T						
	(3) LCD crack	 <p>Crack Not allowed</p>						

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2.0	<p>Spot defect</p> <p>$\Phi = (X+Y)/2$</p>	① light dot (LCD/TP/Polarizer black/white spot , light dot, pinhole, dent, stain)																									
		<table border="1"> <thead> <tr> <th rowspan="2">Zone Size (mm)</th> <th colspan="3">Acceptable Qty</th> </tr> <tr> <th>A</th> <th>B</th> <th>C</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.10$</td> <td colspan="3">Ignore</td> </tr> <tr> <td>$0.10 < \Phi \leq 0.25$</td> <td colspan="3">3(distance $\geq 10\text{mm}$)</td> </tr> <tr> <td>$0.25 < \Phi \leq 0.3$</td> <td colspan="3">2</td> </tr> <tr> <td>$\Phi > 0.35$</td> <td colspan="3">0</td> </tr> </tbody> </table>			Zone Size (mm)	Acceptable Qty			A	B	C	$\Phi \leq 0.10$	Ignore			$0.10 < \Phi \leq 0.25$	3(distance $\geq 10\text{mm}$)			$0.25 < \Phi \leq 0.3$	2			$\Phi > 0.35$	0		
		Zone Size (mm)	Acceptable Qty																								
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$0.25 < \Phi \leq 0.3$	2																										
$\Phi > 0.35$	0																										
② Dim spot (LCD/TP/Polarizer dim dot, light leakage, dark spot)																											
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Zone Size (mm)	Acceptable Qty																										
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$0.3 < \Phi \leq 0.5$	2(distance $\geq 10\text{mm}$)																										
$\Phi > 0.5$	0																										
④ Pixel bad points (light dot, Dim dot, color dot)																											
<table border="1"> <thead> <tr> <th rowspan="2">Zone Size (mm)</th> <th colspan="3">Acceptable Qty</th> </tr> <tr> <th>A</th> <th>B</th> <th>C</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.1$</td> <td colspan="3">Ignore</td> </tr> <tr> <td>$0.15 < \Phi \leq 0.25$</td> <td colspan="3">2(distance $\geq 10\text{mm}$)</td> </tr> <tr> <td>$\Phi > 0.3$</td> <td colspan="3">0</td> </tr> </tbody> </table>			Zone Size (mm)	Acceptable Qty			A	B	C	$\Phi \leq 0.1$	Ignore			$0.15 < \Phi \leq 0.25$	2(distance $\geq 10\text{mm}$)			$\Phi > 0.3$	0								
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ISO/TS 16949:2009
ISO9001:2008



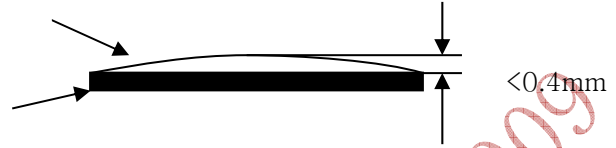
3.0	Line defect (LCD/TP /Polarizer backlight black/white line, scratch, stain)	Width(mm)	Length(m)	Acceptable Qty		
				A	B	C
		$\Phi \leq 0.05$	Ignore	Ignore		
		$0.05 < W \leq 0.06$	$L \leq 3.0$	$N \leq 2$		
		$0.07 < W \leq 0.08$	$L \leq 2.0$	$N \leq 1$		
		$0.08 < W$	Define as spot defect			
4.0	Electronic Components SMT	Not allow missing parts, solderless connection, cold solder joint, mismatch, The positive and negative polarity opposite				
5.0	Display color & Brightness	<p>1. Color: Measuring the color coordinates, The measurement standard according to the datasheet or samples.</p> <p>2. Brightness: Measuring the brightness of White screen, The measurement standard according to the datasheet or Samples.</p>				

6.0	RTP Related	TP film bubble/ accident spot	Size Φ (mm)	Acceptable Qty			
				A	B	C	
			$\Phi \leq 0.1$	Ignore			
			$0.1 < \Phi \leq 0.2$	3 (distance ≥ 10 mm)			
			$0.25 < \Phi \leq 0.3$	2			
				$\Phi > 0.35$	0		
		TP film scratch	Width(mm)	Length(mm)	Acceptable Qty		
					A	B	C
			$\Phi \leq 0.05$	Ignore	Ignore		
			$0.05 < W \leq 0.06$	$L \leq 3.0$	$N \leq 2$		
$0.07 < W \leq 0.08$	$L \leq 2.0$		$N \leq 1$				
		$0.08 < W$	Define as spot defect				
Assembly	beyond the edge of backlight ≤ 0.2 mm						

deflection

Bulge
(undulation
included)

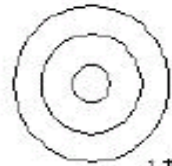
The ITO film plumped below 0.40mm, it's ok.



Newton
Ring

Newton Ring area $> 1/3$ TP area
NG

Newton Ring area $\leq 1/3$ TP area
OK



1 规律性



2 非规律性



似牛顿环

ISO 9001: 2008

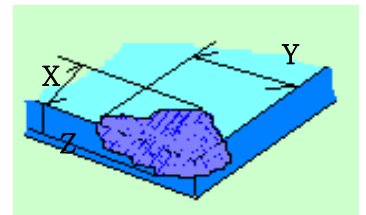
ISO/TS16949 2009

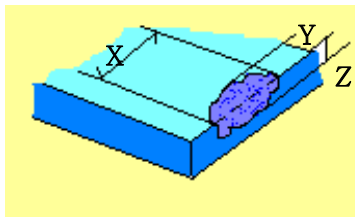
TP corner
broken
X : length
Y : width
Z : height

X	Y	Z
$X \leq 3\text{mm}$	$Y \leq 3\text{mm}$	$Z < \text{COVER thickness}$

*

*Circuitry broken is not allowed.



		TP edge broken X : length Y : width Z : height	X	Y	Z	
			X≤4mm	Y≤2mm	Z<COVER thickness	

Criteria (functional items)

Number	Items	Criteria (mm)
1	No display	Not allowed
2	Missing segment	Not allowed
3	Short	Not allowed
4	Backlight no lighting	Not allowed
5	TP no function	Not allowed

ISO9001: 2008

ISO/TS16949: 2009

8. Reliability Test Result

Item	Condition	Inspection after test
High Temperature Operating	70℃,96H	section after 2~4hours storage at room temperature, the sample shall be free from defects: 1.Air bubble in the LCD; 2.Non-display; 3.Missing segments/line; 4.Glass crack; 5.Current IDD is twice higher than initial value.
Low Temperature Operating	-20℃, 96HR	
High Temperature Storage	80℃, 96HR	
Low Temperature Storage	-30℃, 96HR	
High Temperature & High Humidity Storage	+60℃, 90% RH ,96 hours.	
Thermal Shock (Non-operation)	-30℃,30 min ↔ 80℃,30 min, Change time:5min 20CYC.	
ESD test	C=150pF, R=330,5points/panel Air:±8KV, 5times; Contact:±6KV, 5 times; (Environment: 15℃~35℃, 30%~60%).	
Vibration (Non-operation)	Frequency range:10~55Hz, Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X.Y.Z. (6 hours for total) (Package condition).	
Box Drop Test	1 Corner 3 Edges 6 faces,80cm(MEDIUM BOX)	

Remark:

- The test samples should be applied to only one test item.
- Sample size for each test item is 5~10pcs.
- For Damp Proof Test, Pure water(Resistance > 10MΩ) should be used.
- In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.
- Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

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	常备库存 Standing Stock	长期供货 Long Time supply	支持小量 NO MOQ	品种齐全 In Full Range

9. Cautions and Handling Precautions

9.1 Handling and Operating the Module

- (1) When the module is assembled, it should be attached to the system firmly.
Do not warp or twist the module during assembly work.
- (2) Protect the module from physical shock or any force. In addition to damage, this may cause improper operation or damage to the module and back-light unit.
- (3) Note that polarizer is very fragile and could be easily damaged. Do not press or scratch the surface.
- (4) Do not allow drops of water or chemicals to remain on the display surface.
If you have the droplets for a long time, staining and discoloration may occur.
- (5) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.
- (6) The desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane.
Do not use ketene type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (7) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs, or clothes, it must be washed away thoroughly with soap.
- (8) Protect the module from static; it may cause damage to the CMOS ICs.
- (9) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (10) Do not disassemble the module.
- (11) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (12) Pins of I/F connector shall not be touched directly with bare hands.
- (13) Do not connect, disconnect the module in the "Power ON" condition.
- (14) Power supply should always be turned on/off by the item 6.1 Power On Sequence & 6.2 Power Off Sequence

9.2 Storage and Transportation.

- (1) Do not leave the panel in high temperature, and high humidity for a long time.
It is highly recommended to store the module with temperature from 0 to 35 °C and relative humidity of less than 70%
- (2) Do not store the TFT-LCD module in direct sunlight.
- (3) The module shall be stored in a dark place. When storing the modules for a long time, be sure to adopt effective measures for protecting the modules from strong ultraviolet radiation, sunlight, or fluorescent light.
- (4) It is recommended that the modules should be stored under a condition where no condensation is allowed. Formation of dewdrops may cause an abnormal operation or a failure of the module.
In particular, the greatest possible care should be taken to prevent any module from being operated where condensation has occurred inside.
- (5) This panel has its circuitry FPC on the bottom side and should be handled carefully in order not to be stressed

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10. Packing

----TBD-----

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