

深圳市亿显国际科技有限公司 ShenZhen Yes-Display International Technology CO.,LTD.			2.8 寸液晶显示屏 2.8 Inch LCD Display Screen	
File NO.		REV	A/01	<a href="http://www.yes-display.com">http://www.yes-display.com</a>

# SPECIFICATION FOR

**Module:YS-T0280B46P-02 V1.0**

Designed by	R&D Checked by	Quality Department by	Approved by

## Approval by Customer:

OK

NG, Problem survey

Approved By \_\_\_\_\_



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## Contents

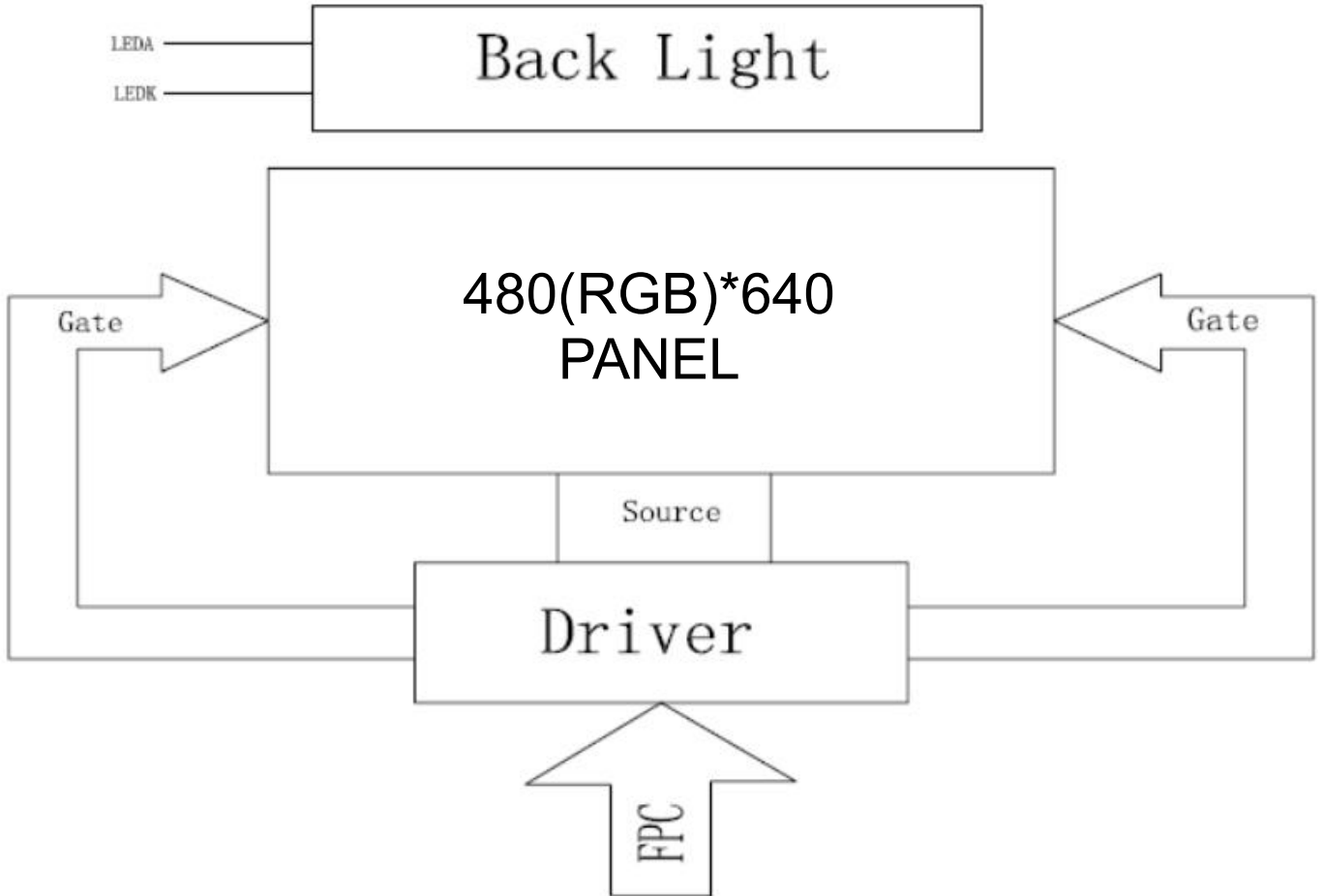
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## 1. Technical parameters

ITEM	STANDARD VALUES	UNITS
LCD type	2.8TFT	--
Dot arrangement	480(RGB)×640	dots
Color filter array	RGB vertical stripe	--
Display mode	IPS / Transmission / Normally Black	-
Eyes Viewing Direction	80/80/80/80	--
Driver IC	ST7701SN	--
Module size	50.00(W)×69.20(H)×2.40(T)(Exclude FPC)	mm
Active area	43.20(W)×57.60(H)	mm
Interface	2 MIPI	--
Operating temperature	-20 ~ +70	°C
Storage temperature	-30 ~ +80	°C
Back Light	White LED*4	--

## 2. Block Dimension





## 4. Input terminal Pin Assignment Description

### 4.1 TFT Pin Description

PIN NO.	PIN NAME	DESCRIPTION
1	GND	System ground
2	LCD_D0N	DSI-DATA- differential data signals.
3	LCD_D0P	DSI-DATA+ differential data signals.
4	GND	System ground
5	LCD_D1N	DSI-DATA- differential data signals.
6	LCD_D1P	DSI-DATA+ differential data signals.
7	GND	System ground
8	LCD_CLKN	DSI-CLK- differential clock signals.
9	LCD_CLKP	DSI-CLK+ differential clock signals.
10	GND	System ground
11-12	NC	NC
13	GND	System ground
14-15	NC	NC
16-17	GND	System ground
18-19	IOVCC(1.8V)	Power supply for logic.
20-21	NC	NC
22	TE	TE signal
23	NC	NC
24	LCD_RESET	Reset signal input terminal, active at 'L'.
25-26	NC	NC
27	GND	System ground
28-29	LEDK	LED Power supply negative
30	GND	System ground
31	NC	NC
32-33	GND	System ground
34	NC	NC
35-36	LEDA	LED Power supply positive

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37	GND	System ground
38-39	VCI	Power supply for analog.
40	NC	NC

## 5. LCD Optical Characteristics

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing Angle	Horizontal	Θ3	CR>10	75	80		°	Note 1
		Θ9		75	80		°	
	Vertical	Θ12		75	80		°	
		Θ6		75	80		°	
Contrast Ratio		CR	Θ= 0°	700	1000			Note 2
Transmittance		%	Θ= 0°	3.9	4.2			With APF
NTSC		%	Θ= 0°	66	71			
Reproduction of color	Red	Rx	Θ= 0°	0.645	0.660	0.675		Note 3 *Only CF With OC With C light
		Ry		0.300	0.315	0.330		
	Green	Gx		0.231	0.246	0.261		
		Gy		0.554	0.569	0.584		
	Blue	Bx		0.128	0.143	0.158		
		By		0.074	0.089	0.104		
White		Wx	Θ= 0°	0.274	0.289	0.304		
		Wy		0.295	0.310	0.325		
Response Time		Tr+Tf	Θ= 0°		30	35	ms	Note 4

### Note:

- Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface (see FIG.1).
- Contrast measurements shall be made at viewing angle of  $\Theta= 0^\circ$  and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (See FIG. 1) Luminance Contrast Ratio (CR) is defined mathematically.

$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

- The color chromaticity coordinates specified in Table1 shall be calculated from the spectral data measured with all pixels first in red, green, blue and white.

Measurements shall be made at the center of the C/F.

Measurement condition is C - light source & Halogen Lamp.

4. The electro-optical response time measurements shall be made as FIG.2 by switching the "data" input signal ON and OFF.

The times needed for the luminance to change from 10% to 90% is  $T_r$  and 90% to 10% is  $T_f$ .

Fig

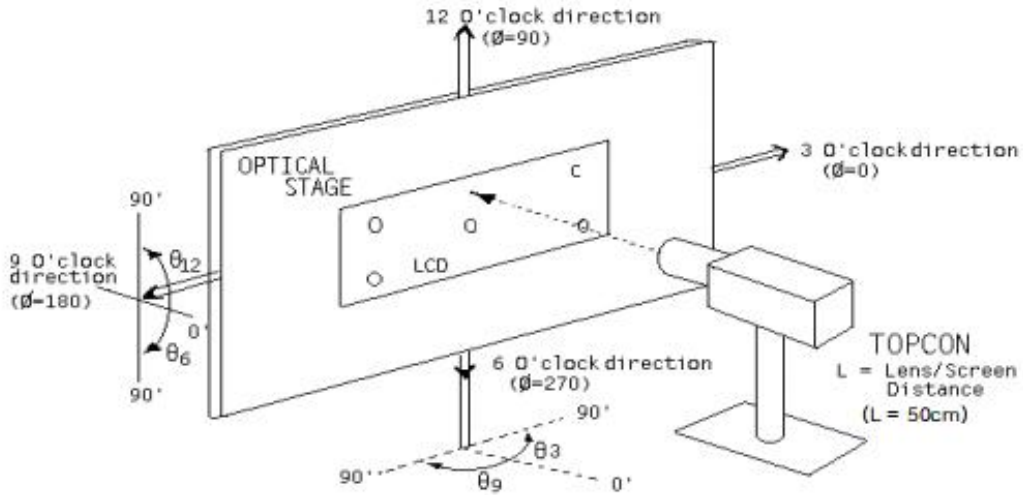
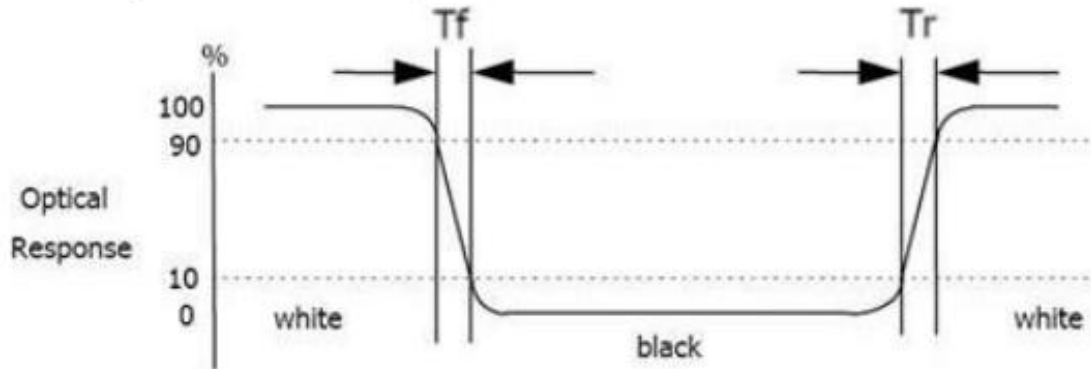


Figure 2. Response Time Testing



## 6. TFT Electrical Characteristics

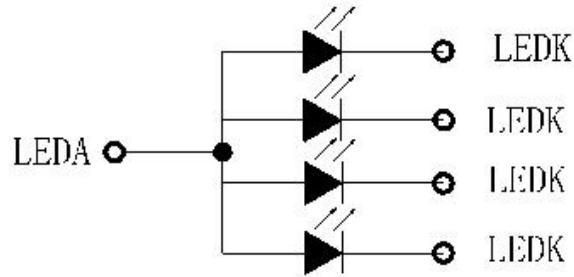
### 6.1 Absolute Maximum Ratings

Item	Symbol	Rating	Unit
Supply Voltage	VDD	- 0.3 ~ +4.6	V
Supply Voltage (Logic)	VDDI	- 0.3 ~ +4.6	V
Driver Supply Voltage	VGH-VGL	-0.3 ~ +30.0	V
Logic Input Voltage Range	VIN	-0.3 ~ VDDI + 0.3	V
Logic Output Voltage Range	VO	-0.3 ~ VDDI + 0.3	V
Operating Temperature Range	TOPR	-30 ~ +85	°C
Storage Temperature Range	TSTG	-40 ~ +125	°C

## 6.2 DC Characteristics

Parameter	Symbol	Condition	Specification			Unit	Related Pins
			MIN.	TYP.	MAX.		
Power & Operation Voltage							
System Voltage	VDD	Operating voltage	2.5	2.8	3.6	V	
Interface Operation Voltage	VDDI	I/O Supply Voltage	1.65	1.8	3.3	V	
Gate Driver High Voltage	VGH		11.5		17	V	
Gate Driver Low Voltage	VGL		-7.6		-12	V	
Gate Driver Supply Voltage		VGH-VGL	-		30	V	
Input / Output							
Logic-High Input Voltage	VIH		0.7VDDI		VDDI	V	Note 1
Logic-Low Input Voltage	VIL		VSS		0.3VDDI	V	Note 1
Logic-High Output Voltage	VOH	IOH = -1.0mA	0.8VDDI		VDDI	V	Note 1
Differential Input High Threshold Voltage	VIT+			0	50	mV	MIPI_CLK MIPI_Data
Differential Input Low Threshold Voltage	VIT-		-50	0		mV	
Single-ended Receiver Input Operation Voltage Range	VIR		0.5		1.2	V	
Logic-Low Output Voltage	VOL	IOL = +1.0mA	VSS		0.2VDDI	V	Note 1
Logic-High Input Current	IIH	VIN = VDDI			1	μA	Note 1
Logic-Low Input Current	IIL	VIN = VSS	-1			μA	Note 1
Input Leakage Current	IIL	IOH = -1.0mA	-0.1		0.1	μA	Note 1
VCOM Voltage							
VCOM amplitude	VCOM			VSS		V	
Source Driver							
Gamma Reference Voltage(Positive)	VAP		4.4		6.4	V	
Gamma Reference Voltage(Negative)	VAN		-2.6		-4.6	V	
Source Output Settling Time	Tr	Below with 99% precision			10	us	Note 2

### 6.3 LED Backlight Characteristics



Item	Symbol	MIN	TYP	MAX	UNIT	Test Condition
Supply Voltage	Vf	2.8	3.2	3.6	V	If=80mA
Supply Current	If	-	80	-	mA	-
Luminous Intensity for LCM	-	-	300	-	cd/m <sup>2</sup>	If=80mA
Uniformity for LCM	-	-	60	-	%	If=80mA
Life Time	-	-	50000	-	Hr	If=80mA
					White	

## 7. Timing Characteristics

### 7.1 TFT Timing Characteristics

#### 7.1.1 MIPI Interface Characteristics:

##### 7.1.1.1 High Speed Mode

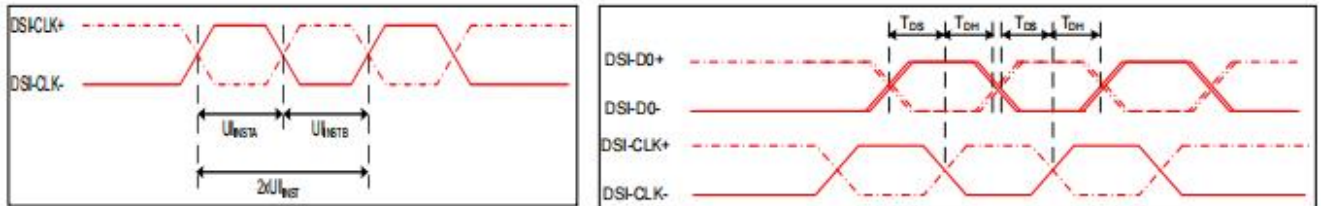


Figure 1 DSI clock channel timing

Figure 2 Rising and falling time on clock and data channel

$V_{DDI}=1.8, V_{DD}=2.8, AGND=DGND=0V, T_a=25\text{ }^{\circ}\text{C}$

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
DSI-CLK+/-	$2xU_{INSTA}$	Double UI instantaneous	2.5	25	ns	
DSI-CLK+/-	$U_{INSTA}$ $U_{INSTB}$	UI instantaneous halves	1.25	12.5	ns	$UI = U_{INSTA} = U_{INSTB}$
DSI-Dn+/-	$t_{DS}$	Data to clock setup time	0.15	-	UI	
DSI-Dn+/-	$t_{DH}$	Data to clock hold time	0.15	-	UI	

Table 1 Mipi Interface- High Speed Mode Timing Characteristics

##### 7.1.1.2 Low Power Mode

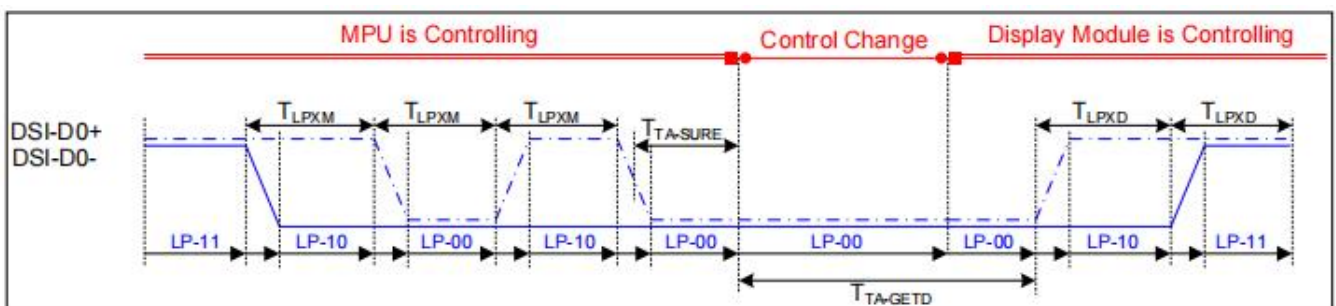


Figure 3 Bus Turnaround (BTA) from display module to MPU Timing

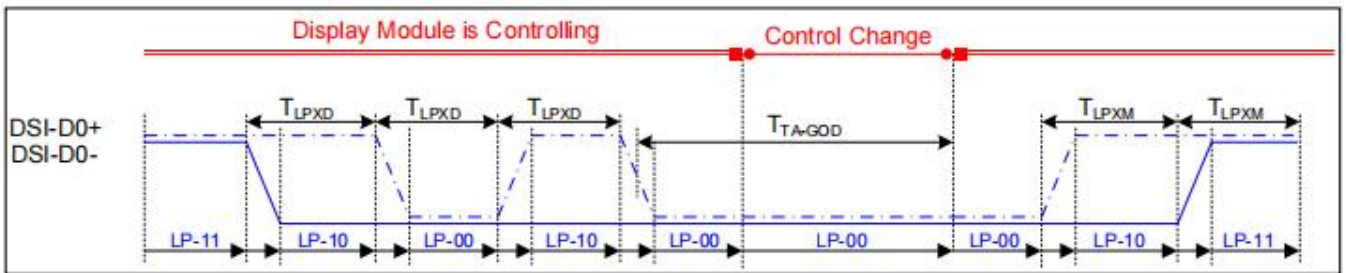


Figure 4 Bus Turnaround (BTA) from MPU to display module Timing

VDDI=1.8,VDD=2.8, AGND=DGND=0V, Ta=25 °C

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
DSI-D0+/-	TLPXM	Length of LP-00,LP-01, LP-10 or LP-11 periods MPU→Display Module	50	75	ns	Input
DSI-D0+/-	TLPXD	Length of LP-00,LP-01, LP-10 or LP-11 periods MPU→Display Module	50	75	ns	Output
DSI-D0+/-	TTA-SURED	Time-out before the MPU start driving	$T_{LPXD}$	$2 \times T_{LPXD}$	ns	Output
DSI-D0+/-	TTA-GETD	Time to drive LP-00 by display module	$5 \times T_{LPXD}$		ns	Input
DSI-D0+/-	TTA-GOD	Time to drive LP-00 after turnaround request-MPU	$4 \times T_{LPXD}$		ns	Output

Table 2 Mipi Interface Low Power Mode Timing Characteristics

### 7.1.1.3 DSI Bursts Mode

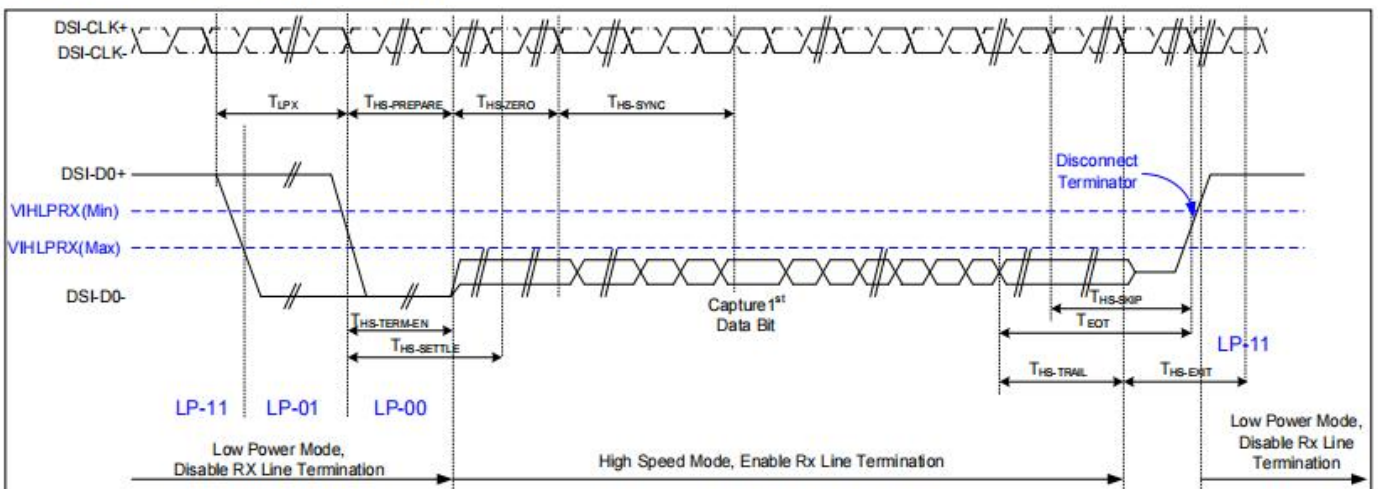


Figure 5 Data lanes-Low Power Mode to/from High Speed Mode Timing

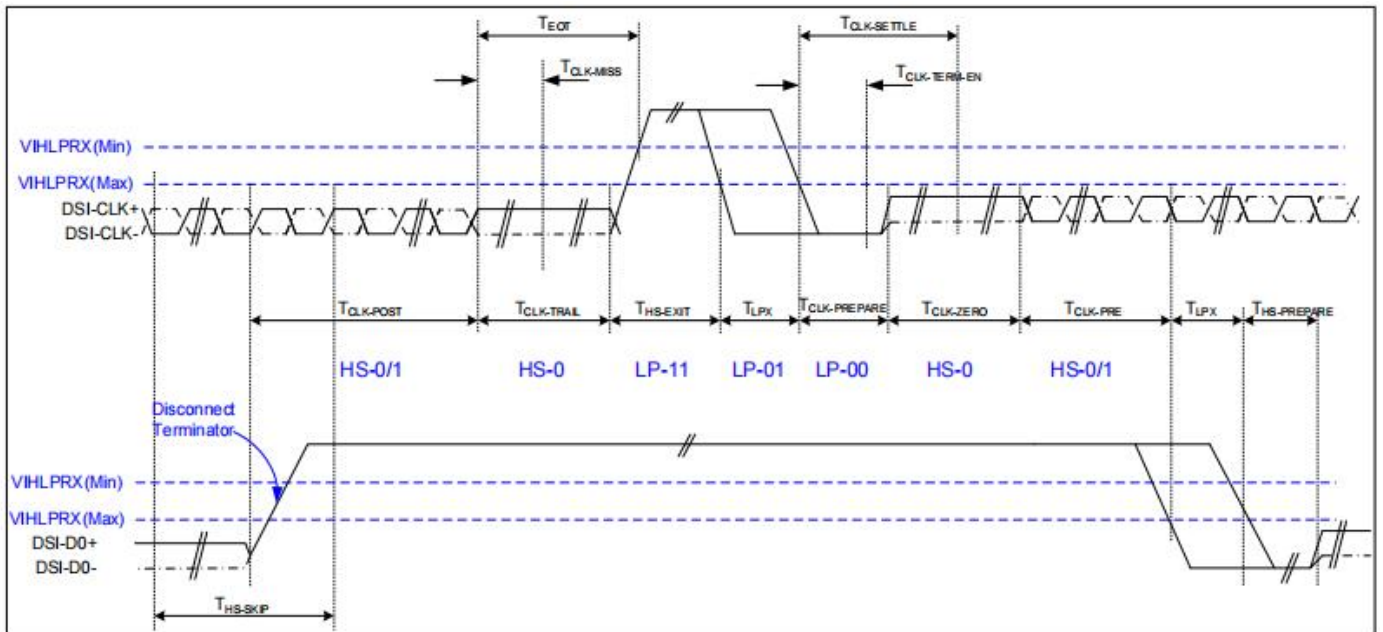


Figure 6 Clock lanes- High Speed Mode to/from Low Power Mode Timing

VDD1=1.8, VDD=2.8, AGND=DGND=0V; T<sub>a</sub>=25 °C

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
Low Power Mode to High Speed Mode Timing						
DSI-Dn+/-	TLPX	Length of any low power state period	50	-	ns	Input
DSI-Dn+/-	THS-PREPARE	Time to drive LP-00 to prepare for HS transmission	40+4 UI	85+6 UI	ns	Input
DSI-Dn+/-	THS-TERM-EN	Time to enable data receiver line termination measured from when Dn crosses VILMAX	-	35+4 UI	ns	Input
DSI-Dn+/-	THS-PREPARE + THS-ZERO	THS-PREPARE + time to drive HS-0 before the sync sequence	140+ 10UI	-	ns	Input
High Speed Mode to Low Power Mode Timing						
DSI-Dn+/-	THS-SKIP	Time-out at display module to ignore transition period of EoT	40	55+4 UI	ns	Input
DSI-Dn+/-	THS-EXIT	Time to drive LP-11 after HS burst	100	-	ns	Input
DSI-Dn+/-	THS-TRAIL	Time to drive flipped differential state after last payload data bit of a HS transmission burst	60+4 UI	-	ns	Input
High Speed Mode to/from Low Power Mode Timing						
DSI-CLK+/-	TCLK-POS	Time that the MPU shall continue sending HS clock after the last associated data lane has transition to LP mode	60+5 2UI	-	ns	Input
DSI-CLK+/-	TCLK-TRAIL	Time to drive HS differential state after last payload clock bit of a HS transmission burst	60	-	ns	Input
DSI-CLK+/-	THS-EXIT	Time to drive LP-11 after HS burst	100	-	ns	Input
DSI-CLK+/-	TCLK-PREPARE	Time to drive LP-00 to prepare for HS transmission	38	95	ns	Input
DSI-CLK+/-	TCLK-TERM-EN	Time-out at clock lan display module to enable HS transmission	--	38	ns	Input
DSI-CLK+/-	TCLK-PREPARE + TCLK-ZERO	Minimum lead HS-0 drive period before starting clock	300	-	ns	Input
DSI-CLK+/-	TCLK-PRE	Time that the HS clock shall be driven prior to any associated data lane beginning the transition from LP to HS mode	8UI	-	ns	Input
DSI-CLK+/-	TEOT	Time form start of TCLK-TRAIL period to start of LP-11 state	-	105n s+12 UI	ns	Input

### 7.1.2 Reset Timing Characteristics

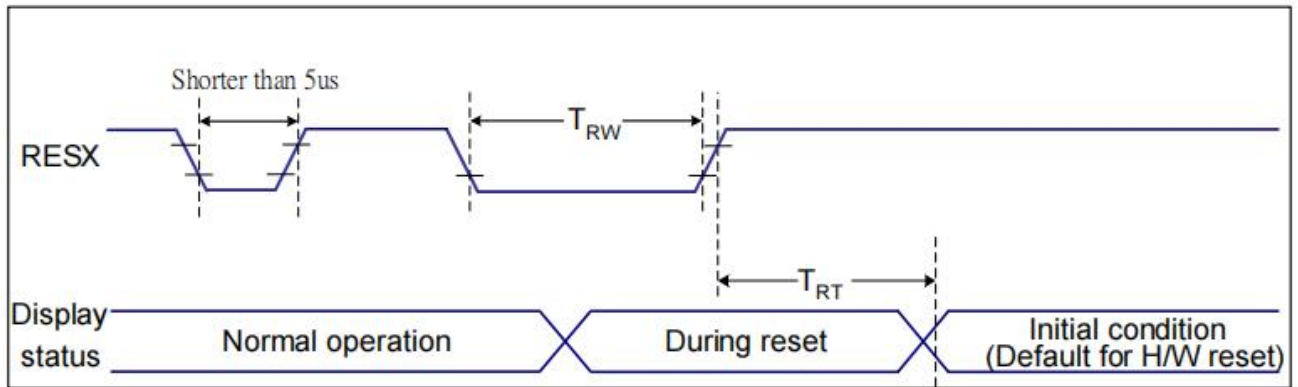


Figure 7 Reset Timing

7.1.3 Power on/off timing sequence check the IC datasheet!

### 7.2 TP Timing Characteristics

TBD

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## 8. Inspection Standard

### 8.1 Incoming Inspection and Standard:

The below incoming inspection are applied to the TFT LCM Modules supplied by ShenZhen Yes-Display International Technology CO.,LTD. The customers should inspect the LCM within 14 days after receiving the goods. The result of inspection should be notified to the Seller in the writing copy promptly, if the customer do not send them within 14 days, the seller has the right to judge as acceptance of goods. The inspection lot size is treated as the quantity per shipment and per model. The sampling plan shall be inspected under MIL-STD015E in Level II by single sampling. The acceptable quality level (AQL) are categorized as below grades:

CRITICAL= 0.4%, MAJOR= 0.65%, MINOR= 1.5%

### 8.2 Inspection condition and Warranty policy:

The delivered LCM should be stored properly, ideally under climate-controlled environment at 25 (±5) degree Celsius as well as 60% (±10) Relative Humidity. The LCM shall be inspected in the viewing angle of 45 degree from the four major angles (U/D/L/R) under the single fluorescent lamp of 20W (equal to 300 to 500 lux). For warranty, ShenZhen Yes-Display International Technology CO.,LTD. will provide 12 months of warranty period as standard, and provide the new replacement for the defective products which belong to the Seller's responsibility verified by the quality department.

### 8.3 Inspection Criteria:

#### 8.3.1 Critical defect

Item No.	Inspection content	Judgement
8.3.1.1	Functional defects	No display, abnormal display, short circuit, missing line, off-contrast and chromaticity, Touch Panel non-function
8.3.1.2	Model mixed	Other model mixed

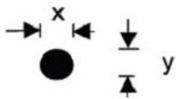
#### 8.3.2 Major defect:

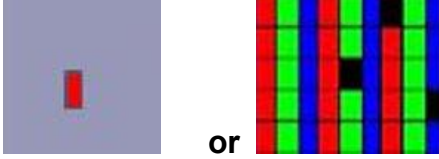
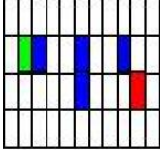
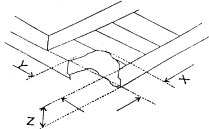
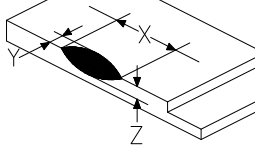
Item No.	Inspection content	Judgement
8.3.2.1	Product indication	Missing model no. and wrong model no. is indicated on the LCM.
8.3.2.2	Glass cracking	The LCD and touch panel glass crack or breakage

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8.3.2.3	Missing component	The function component missing such as connector, cable, etc.
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8.3.3 Minor defect (LCD) :

Item No.	Inspection content	Judgement												
8.3.3.1	Black/White spot Foreign particles Dust in the cell	$\phi = (x+y) / 2$  <table border="1"> <thead> <tr> <th>Diameter (mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.1</math></td> <td>Ignore</td> </tr> <tr> <td><math>0.1 &lt; \Phi \leq 0.25</math></td> <td>3 (Distance&gt;5mm)</td> </tr> <tr> <td><math>0.25 &lt; \Phi</math></td> <td>Not allowed</td> </tr> </tbody> </table>	Diameter (mm)	Acceptable Q'ty	$\Phi \leq 0.1$	Ignore	$0.1 < \Phi \leq 0.25$	3 (Distance>5mm)	$0.25 < \Phi$	Not allowed				
Diameter (mm)	Acceptable Q'ty													
$\Phi \leq 0.1$	Ignore													
$0.1 < \Phi \leq 0.25$	3 (Distance>5mm)													
$0.25 < \Phi$	Not allowed													
8.3.3.2	Linear defect Black/white line Black/white scratch	<table border="1"> <thead> <tr> <th>Length(mm)</th> <th>Width (mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td></td> <td><math>W \leq 0.03</math></td> <td>Ignore</td> </tr> <tr> <td><math>L \leq 5.0</math></td> <td><math>0.03 &lt; W \leq 0.07</math></td> <td>3</td> </tr> <tr> <td></td> <td><math>0.07 &lt; W</math></td> <td>Follow 8.3.3.1</td> </tr> </tbody> </table>	Length(mm)	Width (mm)	Acceptable Q'ty		$W \leq 0.03$	Ignore	$L \leq 5.0$	$0.03 < W \leq 0.07$	3		$0.07 < W$	Follow 8.3.3.1
Length(mm)	Width (mm)	Acceptable Q'ty												
	$W \leq 0.03$	Ignore												
$L \leq 5.0$	$0.03 < W \leq 0.07$	3												
	$0.07 < W$	Follow 8.3.3.1												
8.3.3.3	Polarizer Bubbles Dent on polarizer	<table border="1"> <thead> <tr> <th>Diameter (mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.2</math></td> <td>Ignore</td> </tr> <tr> <td><math>0.2 &lt; \Phi \leq 0.5</math></td> <td>2 (Distance&gt;5mm)</td> </tr> <tr> <td><math>0.5 &lt; \Phi</math></td> <td>Not allowed</td> </tr> </tbody> </table>	Diameter (mm)	Acceptable Q'ty	$\Phi \leq 0.2$	Ignore	$0.2 < \Phi \leq 0.5$	2 (Distance>5mm)	$0.5 < \Phi$	Not allowed				
Diameter (mm)	Acceptable Q'ty													
$\Phi \leq 0.2$	Ignore													
$0.2 < \Phi \leq 0.5$	2 (Distance>5mm)													
$0.5 < \Phi$	Not allowed													

<p>8.3.3.4</p>	<p>Electrical Defect Dot</p>	<p>Bright dot and Dark dot definition:</p>  <p>or</p>  <p>(Two adjacent dot)</p> <p>Inspection pattern: black, white, red, green, and blue screen.</p> <table border="1" data-bbox="730 730 1441 913"> <thead> <tr> <th>Items</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td>Bright dot</td> <td><math>N \leq 4</math> (Distance &gt;5mm)</td> </tr> <tr> <td>Dark dot</td> <td><math>N \leq 4</math> (Distance &gt;5mm)</td> </tr> </tbody> </table>	Items	Acceptable Q'ty	Bright dot	$N \leq 4$ (Distance >5mm)	Dark dot	$N \leq 4$ (Distance >5mm)
Items	Acceptable Q'ty							
Bright dot	$N \leq 4$ (Distance >5mm)							
Dark dot	$N \leq 4$ (Distance >5mm)							
<p>8.3.3.5</p>	<p>Glass Defect- Corner chipping</p>	 <table border="1" data-bbox="730 1104 1441 1375"> <thead> <tr> <th>Size(mm)</th> <th>Judgement</th> </tr> </thead> <tbody> <tr> <td> <math>X \leq 3\text{mm}</math>, <math>Y \leq S</math>,  <math>Z \leq T</math>                      (S= ITO length,                      T=Single glass                      thickness)                 </td> <td>Accept</td> </tr> </tbody> </table>	Size(mm)	Judgement	$X \leq 3\text{mm}$ , $Y \leq S$ , $Z \leq T$ (S= ITO length, T=Single glass thickness)	Accept		
Size(mm)	Judgement							
$X \leq 3\text{mm}$ , $Y \leq S$ , $Z \leq T$ (S= ITO length, T=Single glass thickness)	Accept							
<p>8.3.3.6</p>	<p>Glass Defect- Side fragment</p>	 <table border="1" data-bbox="730 1574 1441 1787"> <thead> <tr> <th>Size(mm)</th> <th>Judgement</th> </tr> </thead> <tbody> <tr> <td> <math>X \leq 2 \text{ mm}</math>, <math>Y \leq \text{border edge}</math>  <math>Z \leq T</math>                      (T= single glass thickness)                 </td> <td>Accept</td> </tr> </tbody> </table>	Size(mm)	Judgement	$X \leq 2 \text{ mm}$ , $Y \leq \text{border edge}$ $Z \leq T$ (T= single glass thickness)	Accept		
Size(mm)	Judgement							
$X \leq 2 \text{ mm}$ , $Y \leq \text{border edge}$ $Z \leq T$ (T= single glass thickness)	Accept							

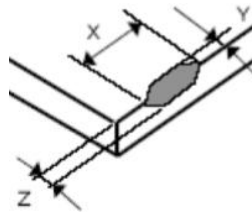
8.3.4 Minor defect (Touch Panel)

Item No.	Inspection content	Judgement
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8.3.4.1	Scratch, dust, particles, foreign materials in "linear type"	<table border="1"> <thead> <tr> <th>Size (mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td><math>W \leq 0.05\text{mm}, L \leq 10\text{mm}</math></td> <td>Ignore</td> </tr> <tr> <td><math>0.05\text{mm} &lt; W \leq 0.07\text{mm}, L \leq 10\text{mm}</math></td> <td>3</td> </tr> <tr> <td><math>W &gt; 0.07\text{mm}</math></td> <td>Reject</td> </tr> </tbody> </table>	Size (mm)	Acceptable Q'ty	$W \leq 0.05\text{mm}, L \leq 10\text{mm}$	Ignore	$0.05\text{mm} < W \leq 0.07\text{mm}, L \leq 10\text{mm}$	3	$W > 0.07\text{mm}$	Reject
Size (mm)	Acceptable Q'ty									
$W \leq 0.05\text{mm}, L \leq 10\text{mm}$	Ignore									
$0.05\text{mm} < W \leq 0.07\text{mm}, L \leq 10\text{mm}$	3									
$W > 0.07\text{mm}$	Reject									
8.3.4.2	Scratch, dust, particles, foreign materials in "round type"	<table border="1"> <thead> <tr> <th>Diameter (mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.25\text{mm}</math></td> <td>Ignore</td> </tr> <tr> <td><math>0.25\text{mm} &lt; \Phi \leq 0.35\text{mm}</math></td> <td>5</td> </tr> <tr> <td><math>\Phi &gt; 0.35\text{mm}</math></td> <td>Reject</td> </tr> </tbody> </table>	Diameter (mm)	Acceptable Q'ty	$\Phi \leq 0.25\text{mm}$	Ignore	$0.25\text{mm} < \Phi \leq 0.35\text{mm}$	5	$\Phi > 0.35\text{mm}$	Reject
Diameter (mm)	Acceptable Q'ty									
$\Phi \leq 0.25\text{mm}$	Ignore									
$0.25\text{mm} < \Phi \leq 0.35\text{mm}$	5									
$\Phi > 0.35\text{mm}$	Reject									
8.3.4.3	Air bubbles	<table border="1"> <thead> <tr> <th>Diameter (mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.2\text{mm}</math></td> <td>Ignore</td> </tr> <tr> <td><math>0.2\text{mm} &lt; \Phi \leq 0.5\text{mm}</math></td> <td>3</td> </tr> <tr> <td><math>\Phi &gt; 0.5\text{mm}</math></td> <td>Reject</td> </tr> </tbody> </table>	Diameter (mm)	Acceptable Q'ty	$\Phi \leq 0.2\text{mm}$	Ignore	$0.2\text{mm} < \Phi \leq 0.5\text{mm}$	3	$\Phi > 0.5\text{mm}$	Reject
Diameter (mm)	Acceptable Q'ty									
$\Phi \leq 0.2\text{mm}$	Ignore									
$0.2\text{mm} < \Phi \leq 0.5\text{mm}$	3									
$\Phi > 0.5\text{mm}$	Reject									
8.3.4.5	Scratch on printing area	<table border="1"> <thead> <tr> <th>Size (mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td><math>W \leq 0.03\text{mm}, L \leq 5\text{mm}</math></td> <td>Ignore</td> </tr> <tr> <td><math>0.03\text{mm} &lt; W \leq 0.05\text{mm}, L \leq 5\text{mm}</math></td> <td>3</td> </tr> <tr> <td><math>W &gt; 0.05\text{mm}</math> or <math>L &gt; 5\text{mm}</math></td> <td>Reject</td> </tr> </tbody> </table>	Size (mm)	Acceptable Q'ty	$W \leq 0.03\text{mm}, L \leq 5\text{mm}$	Ignore	$0.03\text{mm} < W \leq 0.05\text{mm}, L \leq 5\text{mm}$	3	$W > 0.05\text{mm}$ or $L > 5\text{mm}$	Reject
Size (mm)	Acceptable Q'ty									
$W \leq 0.03\text{mm}, L \leq 5\text{mm}$	Ignore									
$0.03\text{mm} < W \leq 0.05\text{mm}, L \leq 5\text{mm}$	3									
$W > 0.05\text{mm}$ or $L > 5\text{mm}$	Reject									
8.3.4.6	Corner chipping	<div data-bbox="730 1541 991 1693" style="text-align: center;"> </div> <table border="1"> <thead> <tr> <th>Size(mm)</th> <th>Judgement</th> </tr> </thead> <tbody> <tr> <td><math>X \leq 2\text{mm}, Y \leq 2\text{mm}</math> <math>Z &lt; 1/2T</math> (T= single glass thickness)</td> <td>Accept</td> </tr> </tbody> </table>	Size(mm)	Judgement	$X \leq 2\text{mm}, Y \leq 2\text{mm}$ $Z < 1/2T$ (T= single glass thickness)	Accept				
Size(mm)	Judgement									
$X \leq 2\text{mm}, Y \leq 2\text{mm}$ $Z < 1/2T$ (T= single glass thickness)	Accept									

8.3.4.7

Edge chipping



Size(mm)	Judgement
$X \leq 3 \text{ mm}$ , $Y \leq 3 \text{ mm}$ $Z \leq 1/2 T$ (T= single glass thickness)	Accept

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## 9. Reliability Test Conditions and Methods

### 9.1 Reliability Test Conditions and Methods:

NO.	TEST ITEMS	TEST CONDITION	INSPECTION AFTER TEST
①	High Temperature Storage	80°C±2°C×96Hours	Inspection after 2~4hours storage at room temperature, the samples should be free from defects: 1, Air bubble in the LCD. 2, Seal leak. 3, Non-display. 4, Missing segments. 5, Glass crack. 6, Current IDD is twice higher than initial value. 7, The surface shall be free from damage. 8, The electric characteristic requirements shall be satisfied.
②	Low Temperature Storage	-30°C±2°C×96Hours	
③	High Temperature Operating	70°C±2°C×96Hours	
④	Low Temperature Operating	-20°C±2°C×96Hours	
⑤	Temperature Cycle(Storage)	-20°C ↔ 25°C ↔ 70°C (30min) ← (5min) → (30min) 1cycle Total 10cycle	
⑥	Damp Proof Test (Storage)	50°C±5°C×90%RH×96Hours	
⑦	Vibration Test	Frequency:10Hz~55Hz~10Hz Amplitude:1.5MM X,Y,Z direction for total 3hours (packing condition test will be tested by a carton)	
⑧	Drooping Test	Drop to the ground from 1M height one time every side of carton. (packing condition test will be tested by a carton)	
⑨	ESD Test	Voltage:±8KV,R:330Ω,C:150PF,Air Mode,10times	

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REMARK:

- 1, The Test samples should be applied to only one test item.
- 2, Sample side for each test item is 5~10pcs.
- 3, For Damp Proof Test, Pure water(Resistance > 10MΩ) should be used.
- 4, In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.
- 5, EL evaluation should be accepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.
- 6, Failure Judgment Criterion: Basic Specification Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

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## 10. Cautions and Handling Precautions

### 10.1 Mounting method

The LCD panel of TFT module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

### 10.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[Recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns

Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (Cl) , Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (Cl), Sulfur (S) from customer, Responsibility is on customer.

### 10.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to power or ground, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

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## 10.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

## 10.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the maximum operating temperature, 50%Rh or less is required.

## 10.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.

[It is recommended to store them as they have been contained in the inner container at the time of delivery from us

## 10.7 Safety

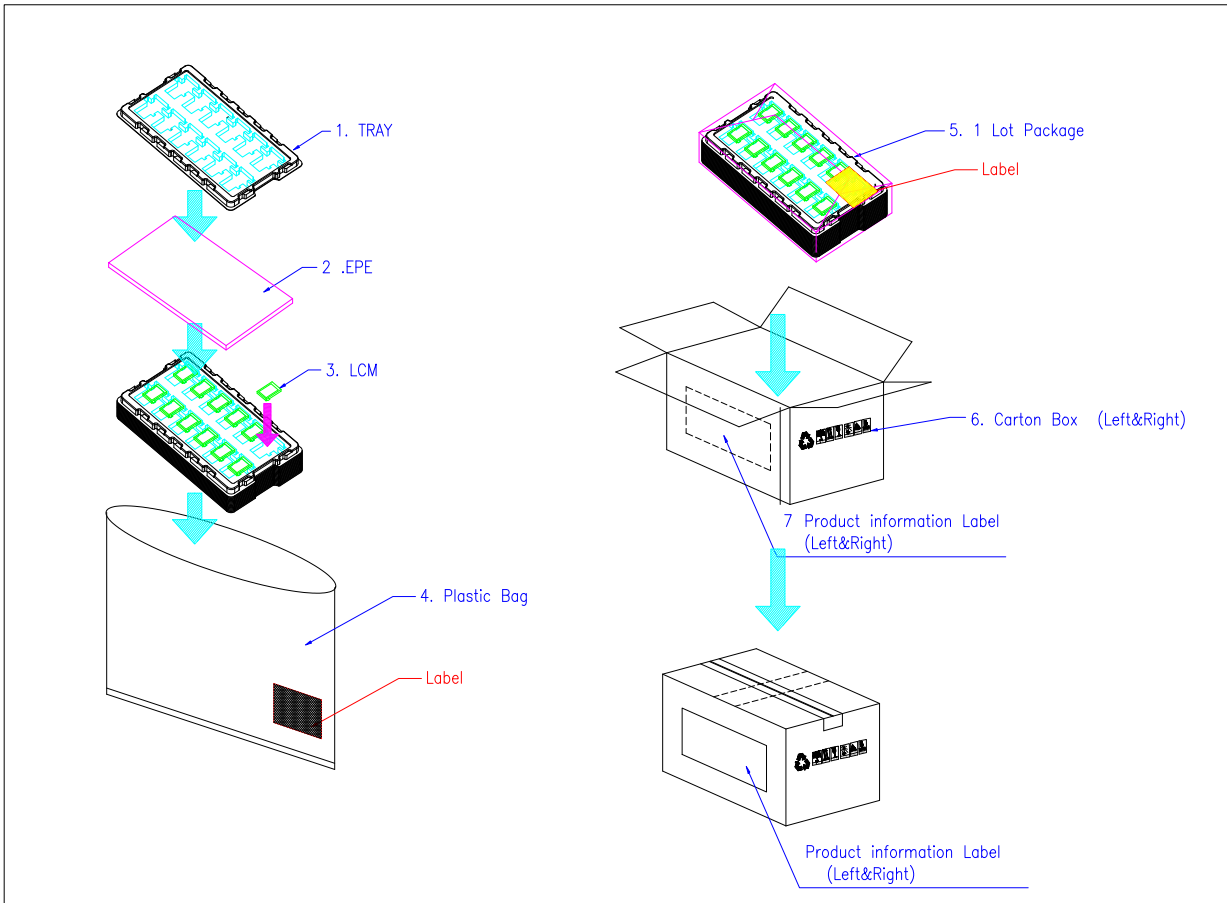
- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.

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- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

## 11. Packing Method

### 11.1 Method



### 11.2 Packing Label

TBD