



东莞市宝龙微电子科技有限公司

DONGGUAN BLW TECHNOLOGY CO., LTD



Product Specification

产品规格书

BLW101SGI02

10.1" 800(H)*3(RGB)*1280(V) TFT LCD
MODULE

MAR, 28, 2018

Customer: _____

Customer Approval and Feedback

BLW Signature:

Prepared by	Checked by	Approved by



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1. GENERAL DESCRIPTION

1.1 DESCRIPTION

BLW101SGI02 is a transmissive type color active matrix TFT (Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT-LCD module (TFT-LCD panel, driver IC and FPC), a back-light unit and. The resolution of 10.1" contains 800X1280 pixels and can display up to 16.7M colors.

1.2 GENERAL INFORMATION

Items	Specification	Unit	Note
Display mode	TFT Transmissive, Positive, NW, IPS	-	-
Drive element	a-Si TFT active matrix	-	-
LCM outline size	143(H) x 228.6(V) x 2.8(T)	mm	Note (1)(2)
Active area	135.36(H)X216.58(V)	mm	-
Number of pixels	800*3RGB(H)X1280(V)	pixels	-
Pixel arrangement	RGB stripe	-	-
Pixel size	0.1692(W) x 0.1692(H)	mm	-
Display color	16.7M	color	-
Viewing direction	ALL	-	-
Controller / Driver	-	-	-
Data interface	MIPI 4LINE	-	-
Backlight	3 White LEDs In Series 9 Ways	-	-
Weight	TBD	g	-

Notes:

- (1) back-light unit are included.
- (2) FPC no included. (Refer to the module outline dimension for further information). Please see module specification drawing in Page10 for more details.



2. ABSOLUTE MAXIMUM RATING

(Ta=25±2°C, Vss=GND=0V)

Characteristics	Symbol	Min.	Typ.	Max.	Unit	Notes
Power Supply Voltage 1	VDDIO	2.8	3	3.3	V	
Power Supply Voltage 2	AVEE	-	-	-	V	
Power Supply Voltage 3	AVDD	-	-	-	V	
Power Supply Voltage 4	IOVCC	-	1.8	-	V	
Power Supply Voltage 5	-	-	-	-	V	
Power Supply Voltage 6	-	-	-	-	V	
TFT Gate On voltage	VGH	-	-	-	V	
TFT Gate Off voltage	VGL	-	-	-	V	
Logic Signal Input Voltage	V _{IN}	-0.3		VDD1+0.3	V	
HS Input Voltage	V _{IN}	-0.3		+2.0	V	
Backlight Forward Voltage	V _f	9.0	9.6	10.5	V	
Backlight Forward Current	I _F	-	180	-	mA	
Operating Temperature	T _{OPR}	-20		+70	°C	(1), (3)
Storage Temperature	T _{STG}	-30		+80	°C	(2), (3)
Humidity	RH	-		90	%	Max. 60 °C

Notes:

- (1) In case of below 0°C, the response time of liquid crystal (LC) becomes slower and the color of panel becomes darker than normal one. Level of retardation depends on temperature, because of the LC characteristics.
- (2) If product is exposed to high temperatures for extended time, there is a possibility of the polarizer film damage which could degrade the optical characteristics.
- (3) Permanent damage to the device may occur if maximum values are exceeded or reverse voltage is loaded.
Functional operation should be restricted to the conditions described under normal operating conditions.



3. ELECTRICAL CHARACTERISTICS

BACK-LIGHT UNIT CHARACTERISTICS

The back-light system is an edge-lighting type with 27white LEDs. The characteristics of the back-light are shown in the following tables.

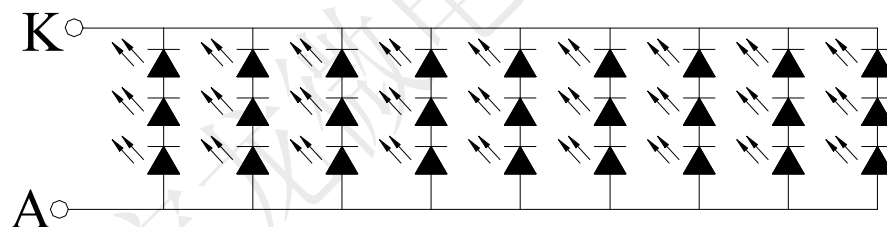
($T_a=25\pm 2^\circ\text{C}$)

Characteristics	Symbol	Condition	Min.	Type	Max.	Unit	Notes
Forward Voltage	Vf	$I_L=180\text{mA}$	9.0	9.6	10.5	V	-
Forward current	I_L		--	180	-	mA	-
Luminance	Lv	$I_L=180\text{mA}$	-	250	--	cd/m ²	-
LED life time	-	$I_L=180\text{mA}$	-	15,000	--	Hr	Note 1

Note:

- (1) The "LED life time" is defined as the module brightness decrease to 50% of original brightness at $I_L=180\text{mA}$. The LED life time could be decreased if operating I_L is larger than 180mA.

Backlight circuit diagram shown in below:



LED CIRCUIT DIAGRAM
 $V_f=9.0\text{V}-10.5\text{V}(I_f=9*20\text{mA})$



4. OPTICAL CHARACTERISTICS

The following items are measured under stable conditions. The optical characteristics should be measured in a dark room.

Measuring equipment: BM-5AS, BM-7, EZ-Contrast.

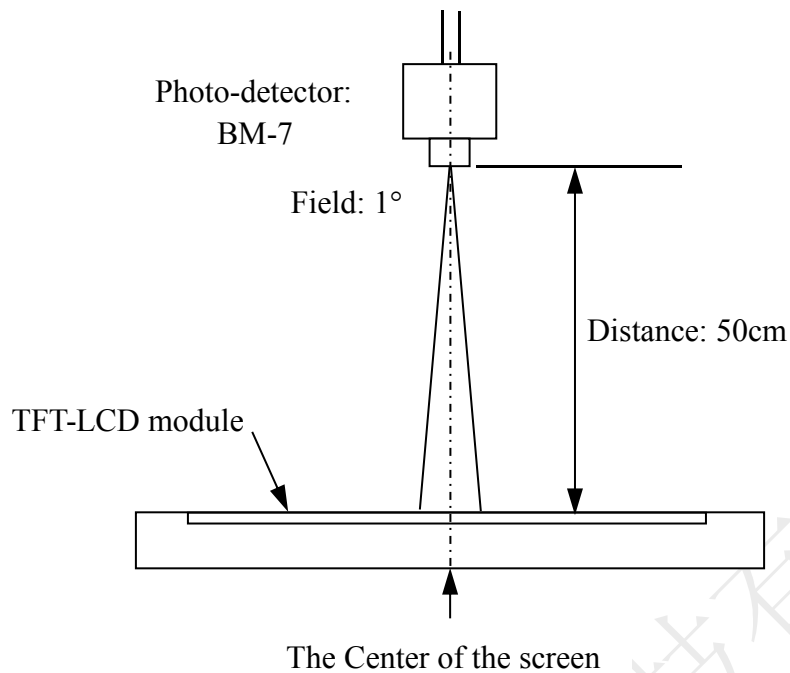
(Ta=25±2°C)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note	
Contrast Ratio (Center point)	C/R	-	500	800	-	-	BM-7 Note(2)	
Luminance of white (Center point)	L _w	B/L on	TBD	350	-	cd/m ²	BM-7	
Luminance uniformity	U _w	θ = 0. Normal viewing angle B/L On Note(1)	80	-	-	%	BM-7 Note(3)	
Response Time	Tr + Tf		-	30	-	ms	BM-5AS Note(4)	
Color Chromaticity (CIE 1931)	White		W _x	0.274	0.304	0.334	-	BM-7 Note(5)
			W _y	0.290	0.320	0.350		
	Red	R _x	0.588	0.618	0.648			
		R _y	0.338	0.368	0.398			
	Green	G _x	0.295	0.325	0.355			
		G _y	0.573	0.603	0.633			
Blue	B _x	0.128	0.158	0.188				
	B _y	0.066	0.096	0.126				
Viewing Angle	Hor.	θ _L	75	80	-	Deg	EZ Contrast Note(6)	
		θ _R	75	80	-			
	Ver.	θ _u	75	80	-			
		θ _D	75	80	-			
Optima View Direction			ALL				Note(7)	

* This condition will be changed by the evaluation circumstance. If product is exposed to high temperatures for extended time, there is a possibility of the polarizer film damage which could degrade the optical characteristics.

Notes:

- (1) Test Equipment Setup: After stabilizing and leaving the panel alone at a given temperature for 30min, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room 30min after lighting the back-light. This should be measured in the center of screen.

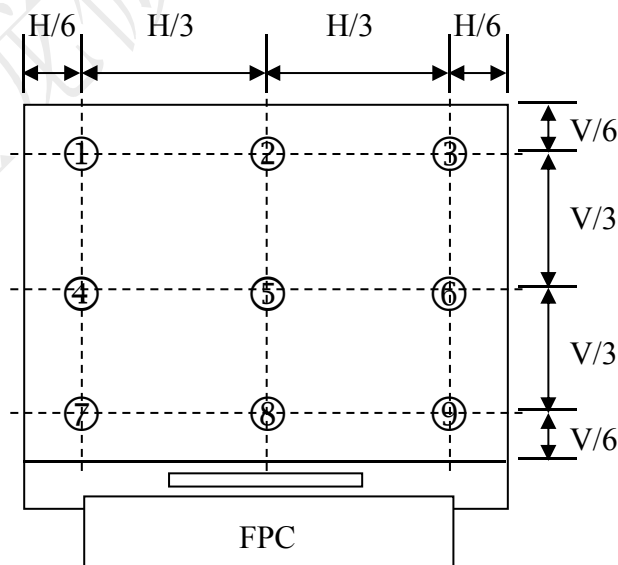


- (2) Definition of Contrast Ratio (CR):

$$\text{Contrast Ratio (CR)} = \frac{\text{Luminance measured when LCD on the "white" state}}{\text{Luminance measured when LCD on the "black" state}}$$

- (3) Definition of Luminance Uniformity: Active area is divided into 9 measuring areas (Shown in below), every measuring point is placed at the center of each measuring area.

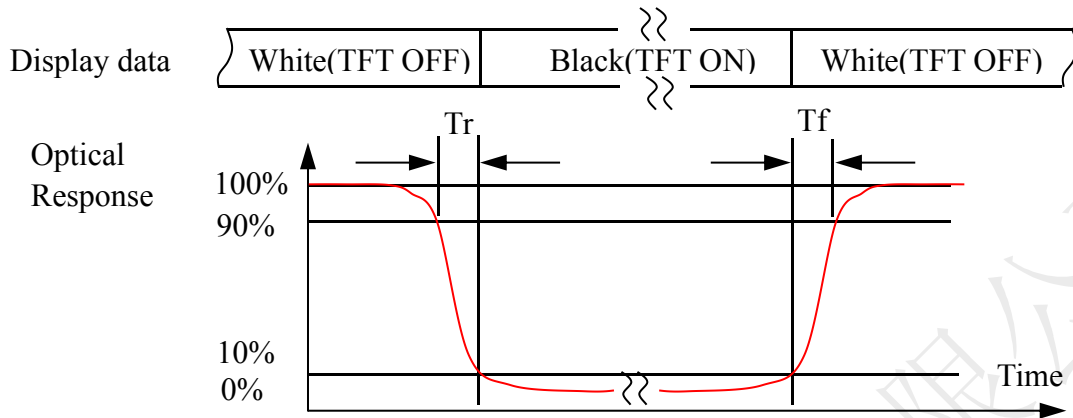
$$\text{Luminance Uniformity} = \frac{\text{Min Luminance of white among 9-points}}{\text{Max Luminance of white among 9-points}} \times 100\%$$



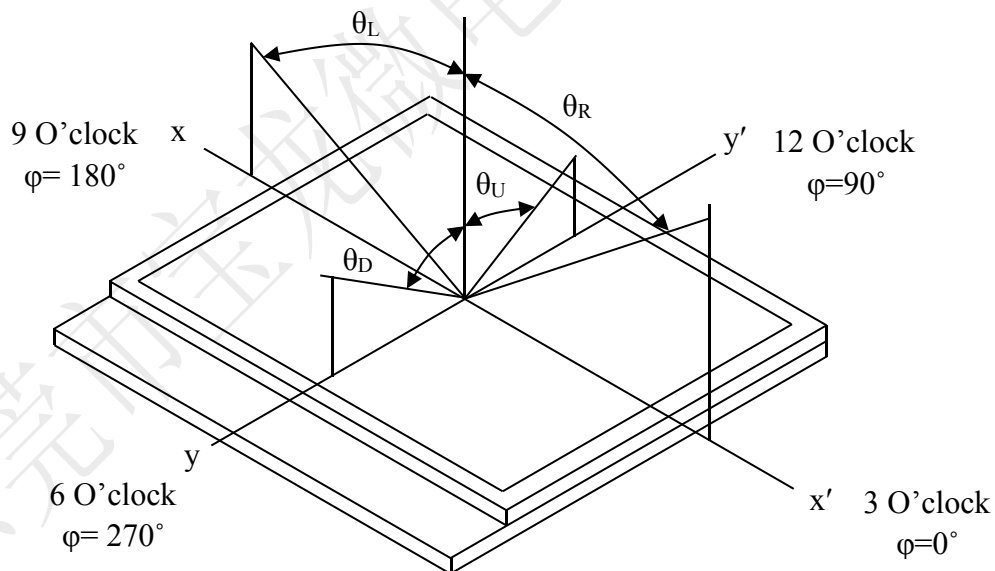
The spot locations for luminance measurement



(4) Definition of Response time: Sum of T_r and T_f .



(5) Definition of Viewing Angle: The viewing angle range that the $CR \geq 10$.



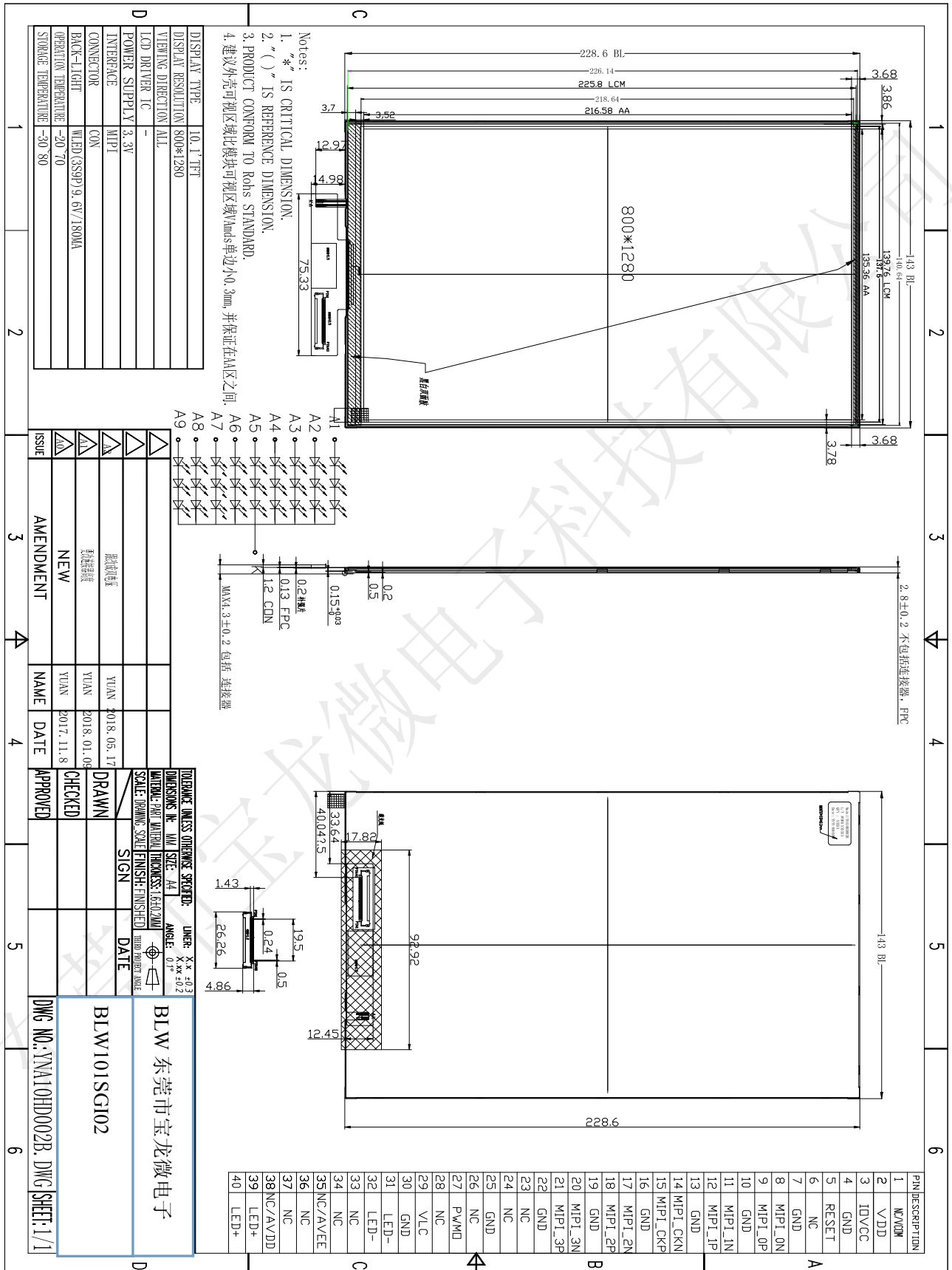
(6) Definition of Color Chromaticity (CIE 1931)

Color coordinate of white & red, green, blue at center point.

(7) The different Rubbing Direction will cause the different optima view direction.



5. MODULE OUTLINE DIMENSION





6. MODULE INTERFACE DESCRIPTION

Pin No.	Symbol	I/O	Function
1	NC /VCOM	P	NC/The power supply of common voltage in DC com driving/NC
2	VDD	P	Power supply for interface system except 3.3V
3	IOVCC	P	Power supply for interface system except 1.8V
4	GND	P	Ground
5	RESET	I	Device reset signa
6	NC	-	No Connection
7	GND	P	Ground
8	MIPI_0N	I/O	MIPI-DSI Data differential signal input pins.
9	MIPI_0P	I/O	MIPI-DSI Data differential signal input pins.
10	GND	P	Ground
11	MIPI_1N	I	MIPI-DSI Data differential signal input pins
12	MIPI_1P	I	MIPI-DSI Data differential signal input pins
13	GND	P	Ground
14	MIPI_CKN	I	MIPI-DSI CLOCK differential signal input pins.
15	MIPI_CKP	I	MIPI-DSI CLOCK differential signal input pins.
16	GND	P	Ground
17	MIPI_2N	I	MIPI-DSI Data differential signal input pins.
18	MIPI_2P	I	MIPI-DSI Data differential signal input pins.
19	GND	P	Ground
20	MIPI_3N	I	MIPI-DSI Data differential signal input pins.
21	MIPI_3P	I	MIPI-DSI Data differential signal input pins.
22	GND	P	Ground
23~24	NC	-	No Connection
25	GND	P	Ground
26	NC	-	No Connection
27	PWMO	I	Backlight on/off control pin/NC
28	NC	-	No Connection
29	VCL	I	negative pressure/NC
30	GND	P	Ground
31~32	LED-	P	Cathode for Backlight
33~34	NC	-	No Connection
35	NC/AVEE	-	NC /NC-5
36~37	NC	-	No Connection
38	NC/AVDD	-	NC/NC+5
39~40	LED+	P	Back-light Anode



7. REFERENCE APPLICATION CIRCUIT

Please consult our technical department for detail information.

8. Data Input Format

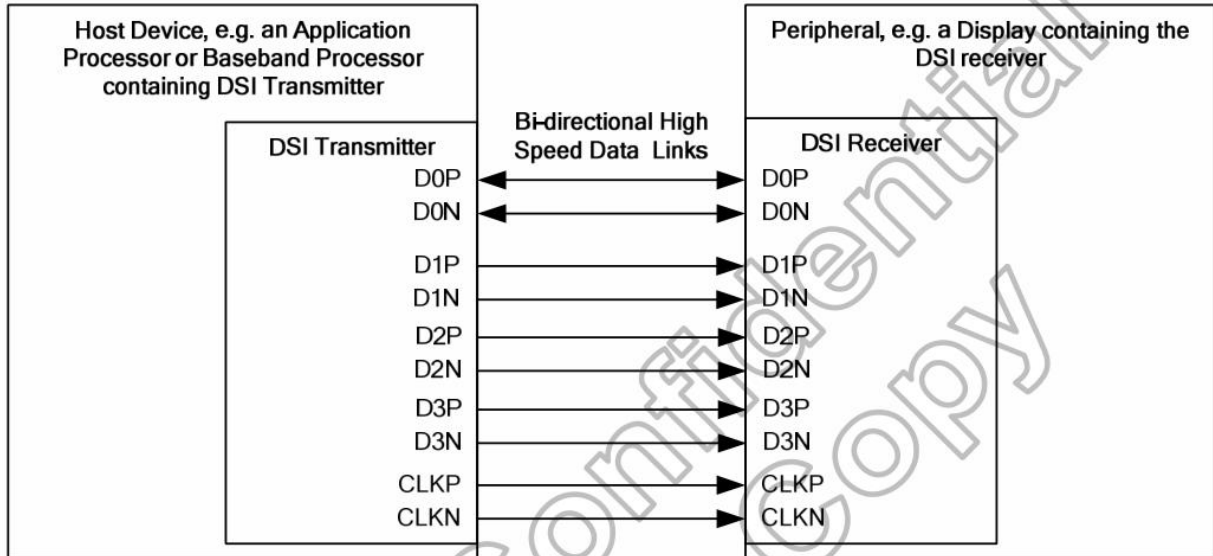


Figure 4.5: DSI transmitter and receiver interface

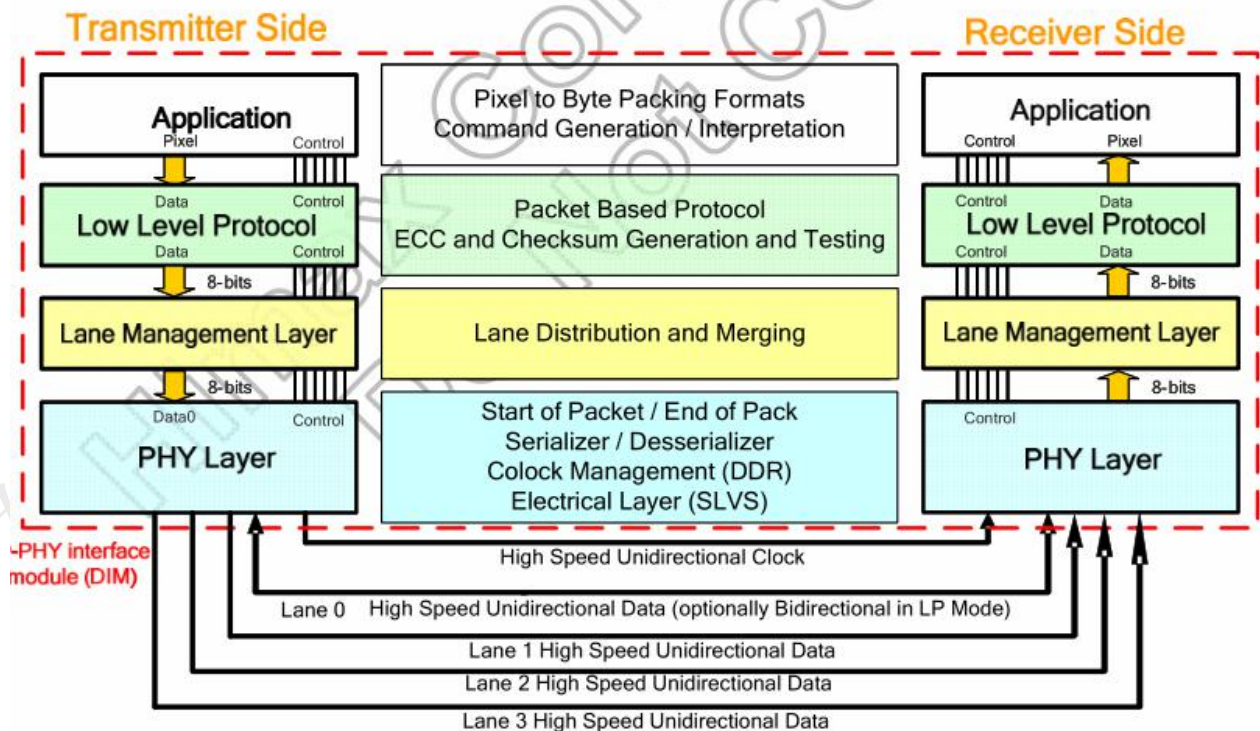


Figure 4.6: DSI transmitter and receiver interface



9. RELIABILITY TEST CONDITIONS

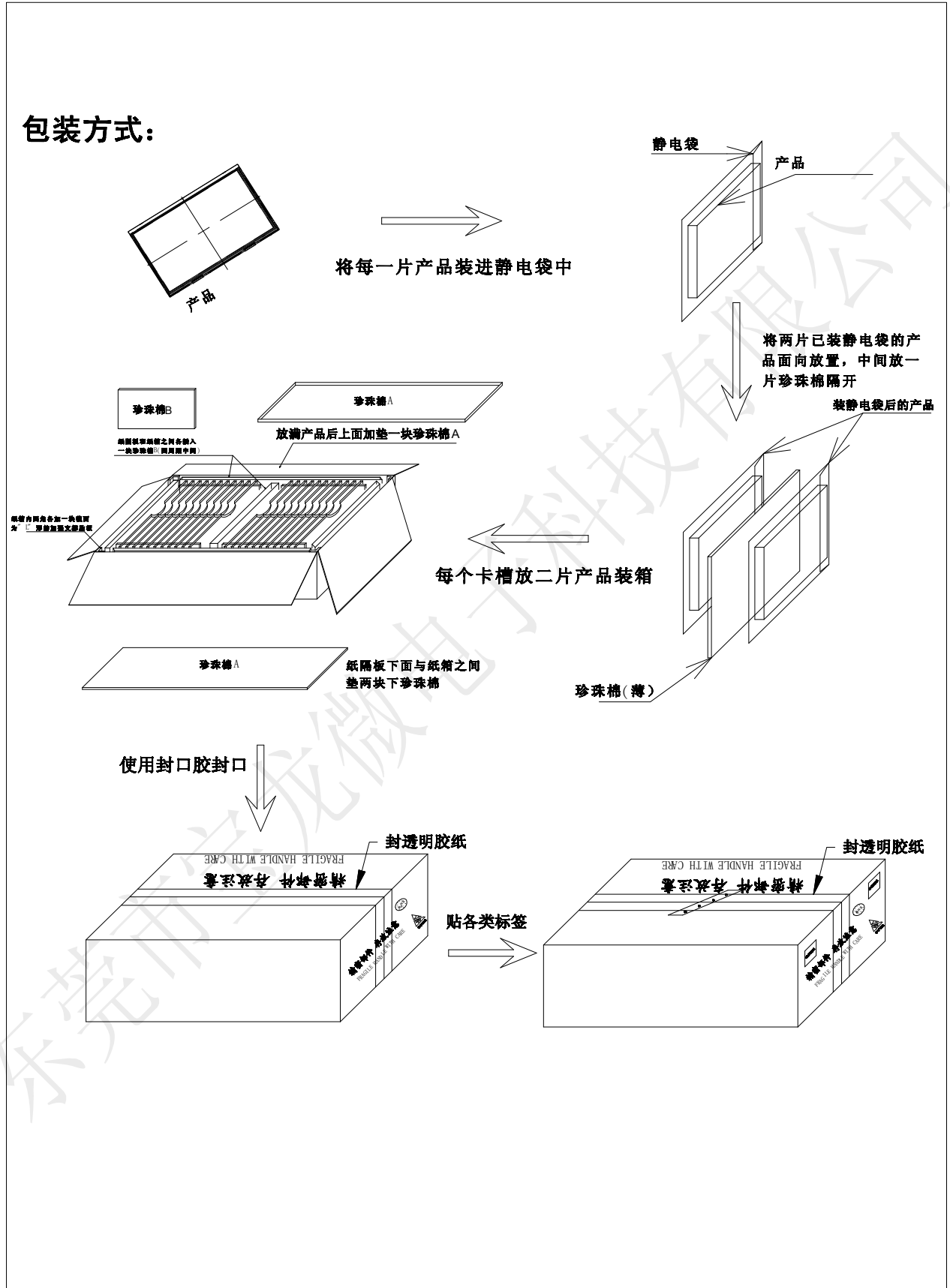
No.	Test Item	Test Condition	Notes
1	High Temperature Storage	+80°C / 240H	Inspection after 2~4h storage at room temperature, the sample shall be free from defects: 1. Air bubble in the LCD; 2. Seal leak; 3. Non-display; 4. Missing segments; 5. Glass crack; 6. The surface shall be free from damage. 7. The electrical characteristics requirements shall be satisfied.
2	Low Temperature Storage	-30°C / 240H	
3	High Temperature Operating	+70°C / 240H	
4	Low Temperature Operating	-20°C / 240H	
5	Temperature Cycle	0±2°C↔25°C↔+50±2°C x 10cycles (30min) (5min) (30min)	
6	High Temperature /Humidity storage	60°C x 90%RH / 120H	
7	Vibration Test	Frequency: 10Hz~55Hz~10Hz Amplitude: 1.5mm, 2 hours for each direction of X, Y, Z	
8	Packing Drop Test	Drop to the ground from 1m height, 1 corner, 3 edges, 6 surfaces.	
9	ESD test	Voltage:±8KV R: 330Ω C: 150pF Air discharge, three or five time	

Remarks:

- (1) The test samples should be applied to only one test item.
- (2) Sample size for each test item is 5~10pcs.
- (3) For High Temperature/Humidity storage test, pure water (resistance>10MΩ) should be used.
- (4) In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.
- (5) Failure judgment criterion: basic specification, electrical characteristic, mechanical characteristic, optical characteristic.

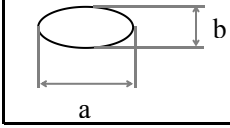
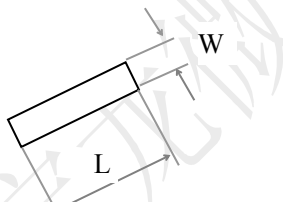
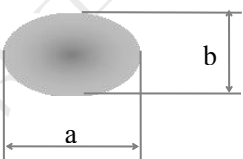


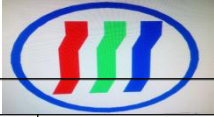
10. PACKING SPECIFICATION




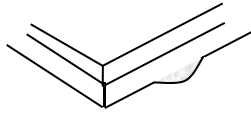



11. INSPECTION CRITERION

Inspection item		Judgement standard					
		Category		Acceptable number			
				A zone	B zone		
1	Black spot, White spot, Bright Spot, Pinhole Foreign Particle, Bubble and Particle Between polarizer and glass, scratch on polarizer	 $\Phi=(a+b)/2(\text{mm})$		A B C	$\Phi \leq 0.10$ $0.10 < \Phi \leq 0.30$ $0.30 < \Phi$	Ignored 2 0	Ignored
	Total defective point(B,C)		2				
	Bright spot	$0.15 < \Phi \leq 0.30$		$N \leq 2$		Ignored	
	Dark spot/ Black spot	$0.15 < \Phi \leq 0.30$		$N \leq 2$			
	Attached to the two pixels are bright spots	$0.15 < \Phi \leq 0.30$		$N \leq 2$			
	Even a two pixel is dark	$0.15 < \Phi \leq 0.30$		$N \leq 2$			
Pixel total number	$0.15 < \Phi \leq 0.30$		$N \leq 2$				
Note1: the spot defect caused by foreign matter is judged according to the defect of the foreign body. Note 2: when the light is not wired to show the type of defects.							
2	Black line, White line, Bubble and Particle Between Polarizer and glass, Scratch on polarizer	 W:Width, L:Length(mm)		A B C D	$W \leq 0.01$ $0.01 < W \leq 0.03 \quad L \leq 3.0$ $0.03 < W \leq 0.05 \quad L \leq 3.0$ $0.05 < W$	Ignored 2 1 0	Ignored
	Total defective point(B,C)		2				
3	Contrast variation	 $\Phi=(a+b)/2(\text{mm})$		A B C	$\Phi \leq 0.1$ $0.1 < \Phi \leq 0.3$ $0.3 < \Phi$	Ignored 2 0	Ignored
	Total defective point(B,C)		2				
4	Bubble inside cell	any size		none	none		
5	Polarizer defect (if Polarizer is used)	Scratch and damage on polarizer, particle on polarizer or between polarizer and glass.		Refer to item 1 and item 2.			
	Bubble, dent and convex	A B C	$\Phi \leq 0.1$ $0.1 < \Phi \leq 0.3$ $0.3 < \Phi$	Ignored 3 0	Ignored		



		Total defective point(B,C)	3	
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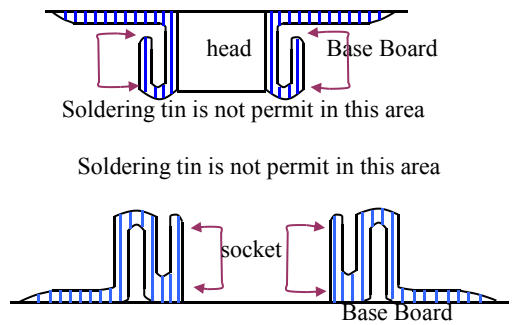
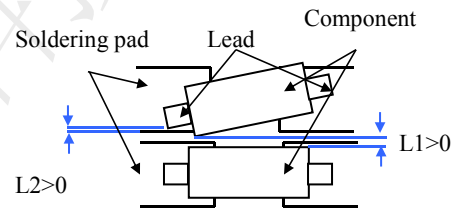
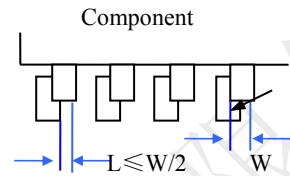
Inspection item		Judgement standard		
		Category	Acceptable number	
			A zone	B zone
6	Surplus glass	①Stage surplus glass 	$b \leq 0.3\text{mm}$	
		②Surrounding surplus glass 	Should not influence outline dimension and assembling.	
7	MURA	①MURA	<p>Naked eye examination: red, green, blue screen does not allow the appearance, black screen requires visual is not obvious, the specific reference limit samples. Note: the principle of closing the sample is to be installed on the whole machine and the end user will not find it in the normal usage scenario. Inspection basis: 6%ND (MURA mainly in the black screen and indoor light is relatively dark will be found, it is recommended to turn off the indoor lighting inspection.)</p>	
		②Point Black / White / point(MURA) I	<p>1, under the black / gray screen check: $D \leq 0.10\text{mm}$ Ignored; $0.10\text{mm} < D \leq 0.3\text{mm}$, $N \leq 2$; $D > 0.3\text{mm}$: Unqualified. 2, switch to the red, green, blue in which any one of the screen appears black or white or point to point white or point of failure.</p> 	



Inspection item		Judgment standard									
		Category(application: B zone)									
8	Glass defect crack	①The front of lead terminals	<table border="1"> <tr> <td>A</td> <td>If $a \leq t$ and $b \leq 1.0$, c is not limited</td> </tr> <tr> <td>B</td> <td>$a \leq t$, $1 \leq b \leq 2\text{mm}$, $c \leq 3\text{mm}$</td> </tr> <tr> <td>C</td> <td>If glass crack cover alignment mark, $b \leq 0.5\text{mm}$.</td> </tr> <tr> <td>D</td> <td>Crack at two sides of lead terminals should not cover patterns and alignment mark</td> </tr> </table>	A	If $a \leq t$ and $b \leq 1.0$, c is not limited	B	$a \leq t$, $1 \leq b \leq 2\text{mm}$, $c \leq 3\text{mm}$	C	If glass crack cover alignment mark, $b \leq 0.5\text{mm}$.	D	Crack at two sides of lead terminals should not cover patterns and alignment mark
		A	If $a \leq t$ and $b \leq 1.0$, c is not limited								
		B	$a \leq t$, $1 \leq b \leq 2\text{mm}$, $c \leq 3\text{mm}$								
		C	If glass crack cover alignment mark, $b \leq 0.5\text{mm}$.								
D	Crack at two sides of lead terminals should not cover patterns and alignment mark										
②Surrounding t crack—non-contact side	<p>$b < \text{Inner borderline of the seal}$</p>										
③ Surrounding crack— contact side	<p>$b < \text{Outer borderline of the seal}$</p>										
④Corner	<table border="1"> <tr> <td>A</td> <td>$a \leq t$, $b \leq 3.0$, $c \leq 3.0$</td> </tr> </table> <p>*Glass crack should not cover patterns used for</p>	A	$a \leq t$, $b \leq 3.0$, $c \leq 3.0$								
A	$a \leq t$, $b \leq 3.0$, $c \leq 3.0$										



Inspection item		Judgement standard
9	FPC defect	<p>Component soldering: No cold soldering, short/open circuit, burr, tin ball.</p> <p>The flat encapsulation component position deviation must be less than 1/2 width of the pin (Pic.1);</p> <p>The sheet component deviation: pin deviates from the pad and contact with the near components is not permitted (Pic.2)</p>
	lead defect:	<p>The lead lack must be less than 1/2 of its width;</p> <p>The lead burr must be less than 1/2 of the seam;</p> <p>Impurities connect with the near leads is not permitted</p>
	Connector soldering:	<p>Soldering tin is at contact position of the plug and socket is not permitted</p> <p>No foundation is scald</p> <p>Serious cave distortion on plug and socket contact pin is not permitted</p>





12. GENERAL PRECAUTIONS

13. HANDING

- (1) When the module is assembled, it should be attached to the system firmly. Be careful not to twist and bent the module.
- (2) Refrain from strong mechanical shock and / or any force to the module. In addition to damage, this may cause improper operation or damage to the module and back-light unit.
- (3) Note that display modules are very fragile and could be easily damaged. Do not press or scratch the surface harder than a HB pencil lead.
- (4) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, straining and discoloration may occur.
- (5) If the display module surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, should be wiped by moisten cloth with isopropyl alcohol or ethyl alcohol solvents, DO NOT with water, ketone type materials (e.g. acetone), aromatic, toluene, ethyl acid or methyl chloride, and so on.
- (6) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs or clothes, it must be washed away thoroughly with soap.
- (7) Use finger-stalls with sort gloves in order to keep display clean during the incoming inspection and assembly process.
- (8) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (9) Do not touch directly conductive parts such as the CMOS LSI pad and the interface terminals with bare hands, therefore operations should be grounded whenever he/she comes into contact with the modules.
- (10) Do not exceed the absolute maximum rating value. (The supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on), otherwise the module may be damaged.

1.1 SOLDERING

- (1) Use soldering irons with proper grounding and no leakage.
- (2) For No RoHS Product: soldering temperature is 290~350°C, soldering time is 3~5s; for RoHS Product: soldering temperature is 340~370°C, soldering time is 3~5s.
- (3) If soldering flux is used, be sure to remove any remaining flux after soldering (This does not apply in the case of a non-halogen type of flux).

1.2 STORAGE

- (1) DO NOT leave the module in high temperature and high humidity for a long times, keep the temperature from 0°C to 35°C and relative humidity of less than 60%.
- (2) It is highly recommended to store the module in a dark place. The Liquid crystal is deteriorated by ultraviolet, DO NOT leave it in direct sunlight and strong ultraviolet ray for many hours.



- (3) The polarizer surface should not come in contact with any other objects.

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